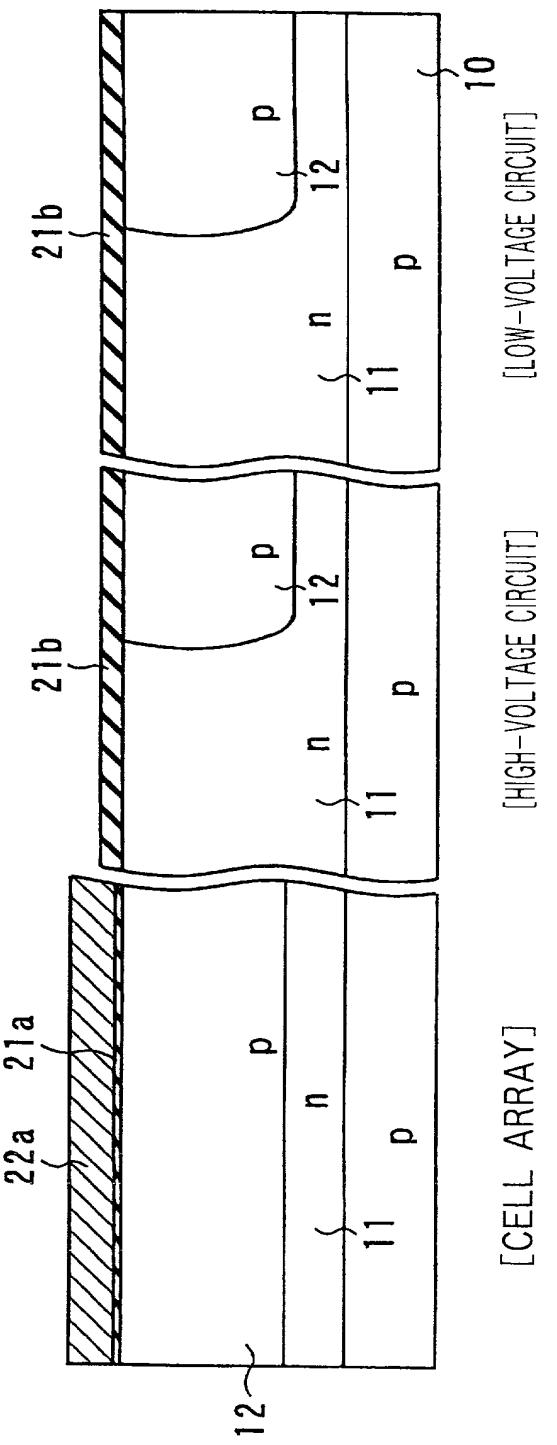


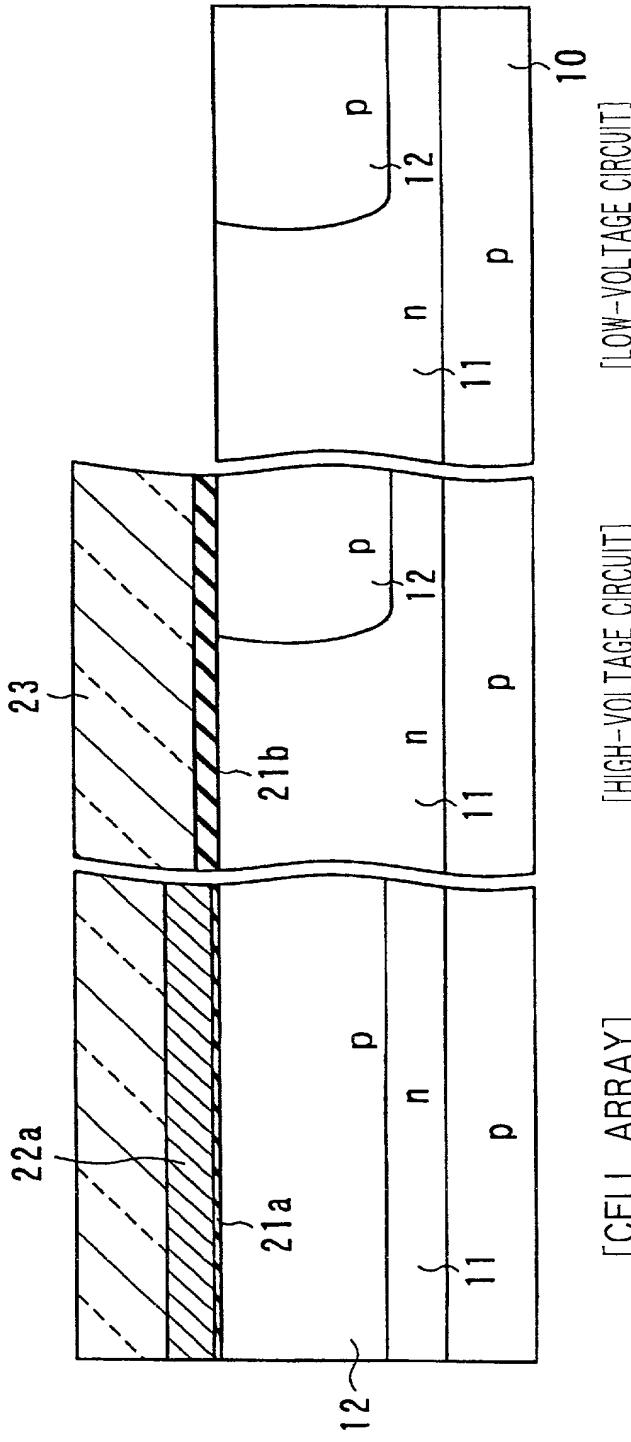
FIG. 1

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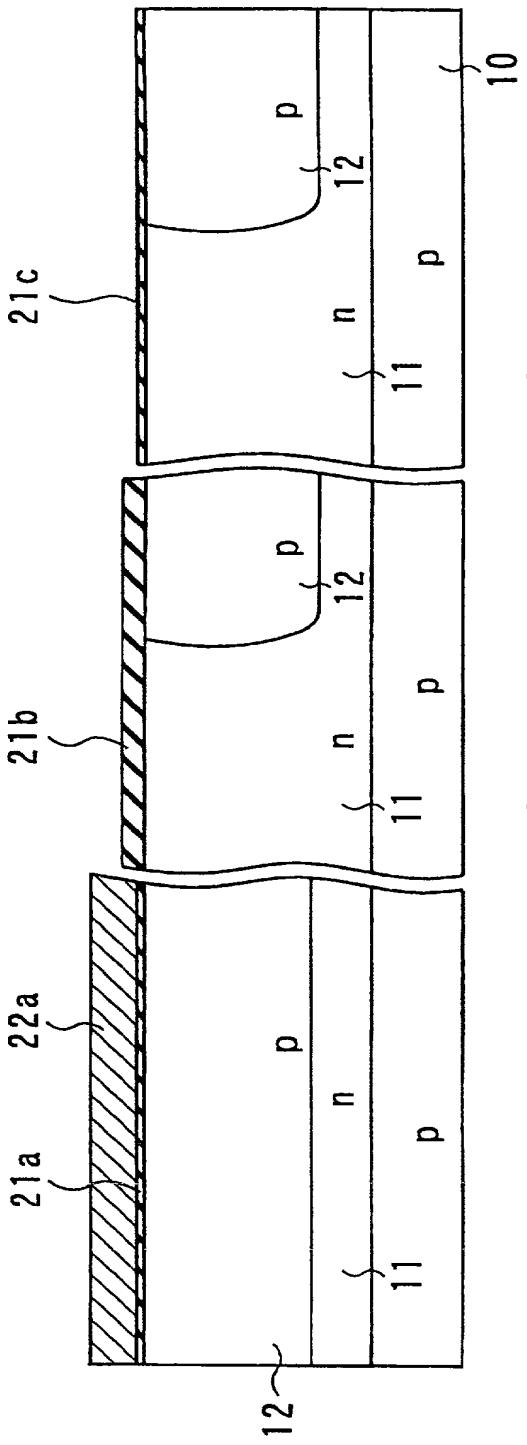


[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT]

[LOW-VOLTAGE CIRCUIT]

FIG. 3

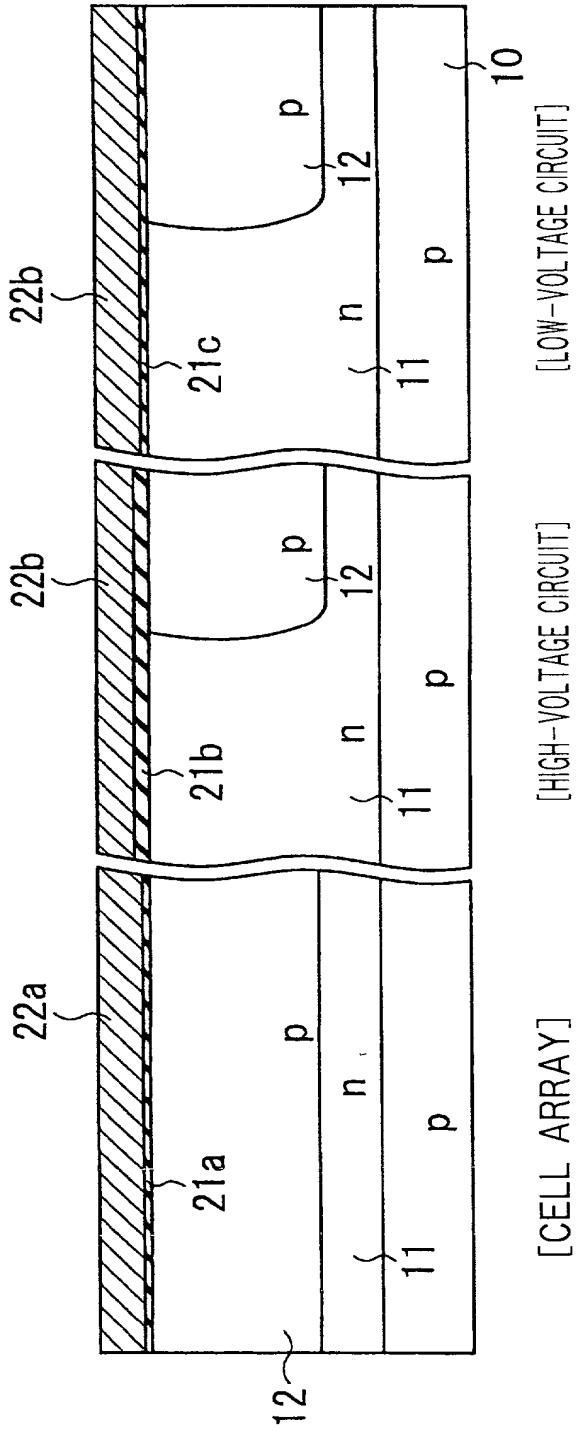
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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 4

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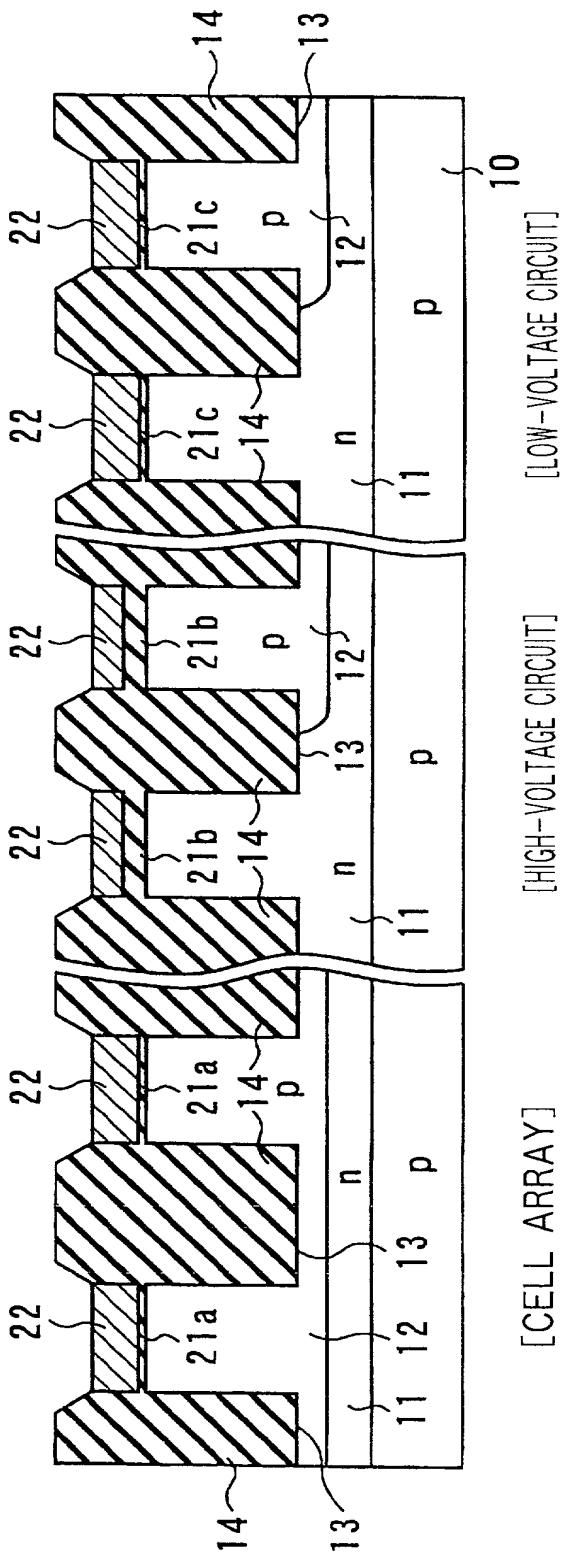


[HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

[CELL ARRAY]

FIG. 5

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[LOW-VOLTAGE CIRCUIT]

[HIGH-VOLTAGE CIRCUIT]

[CELL ARRAY]

FIG. 6

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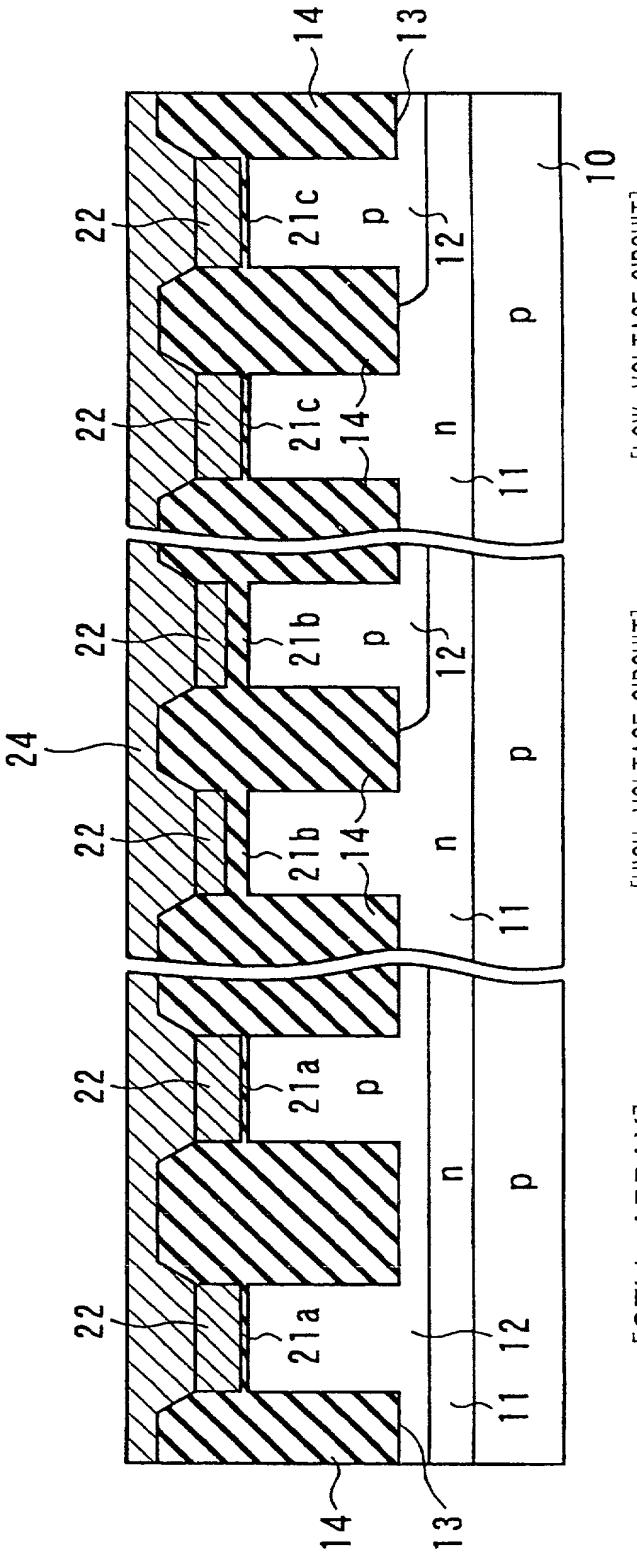
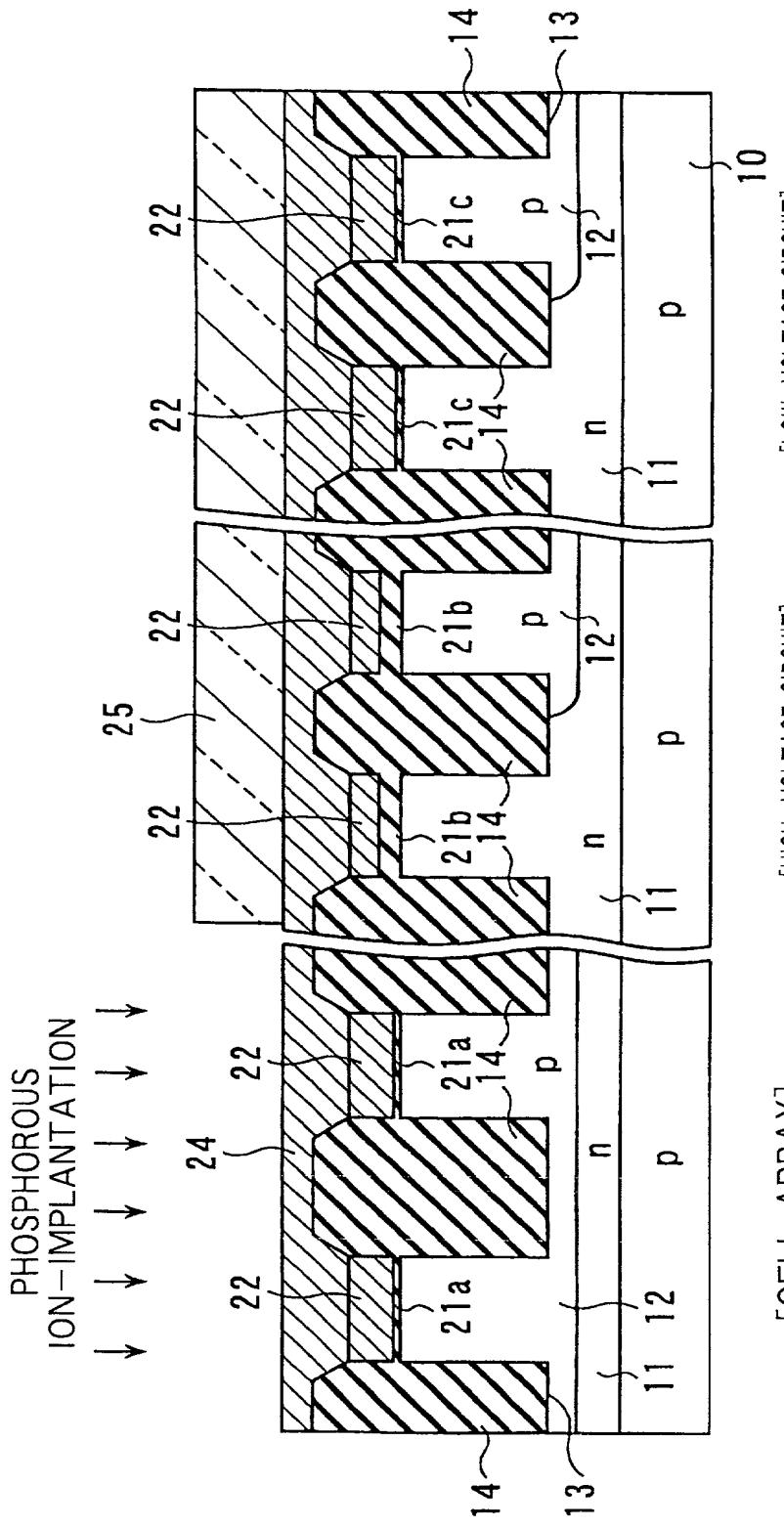


FIG. 7
[CELL ARRAY]
[HIGH-VOLTAGE CIRCUIT]
[LOW-VOLTAGE CIRCUIT]

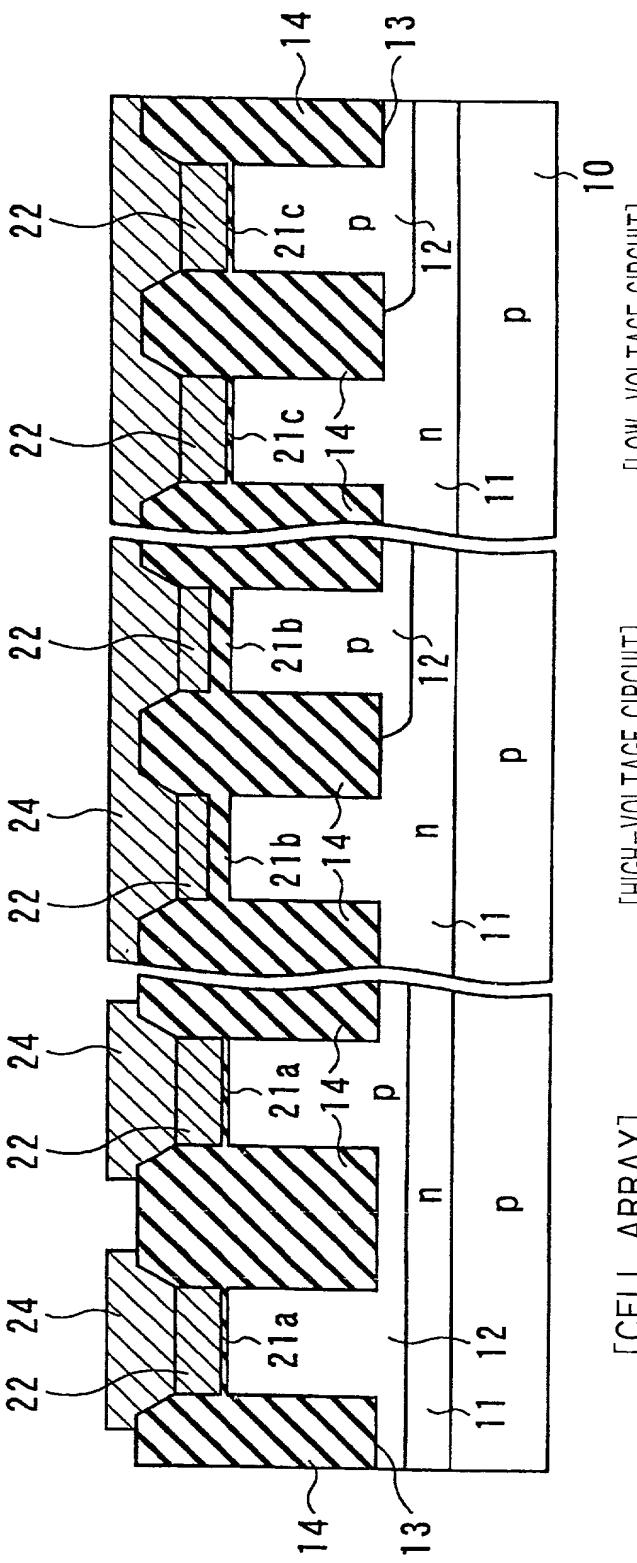
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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

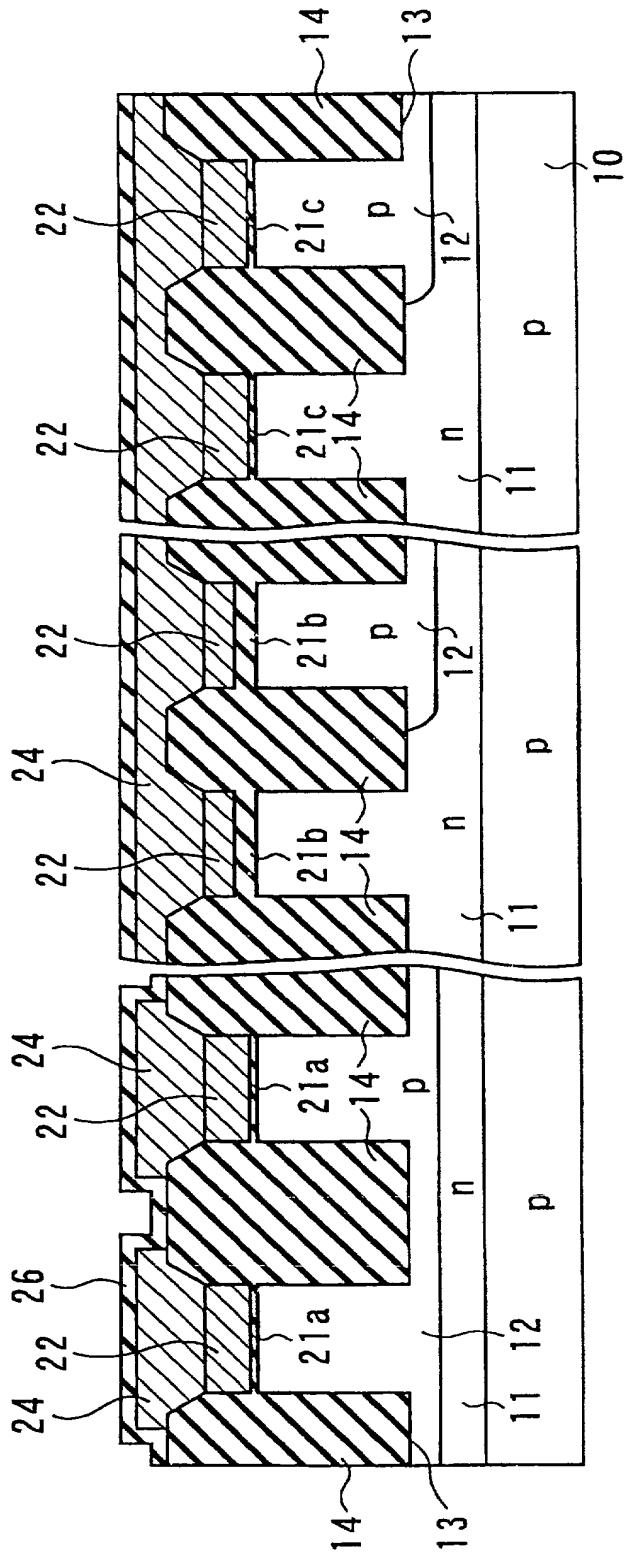
FIG. 8

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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 9

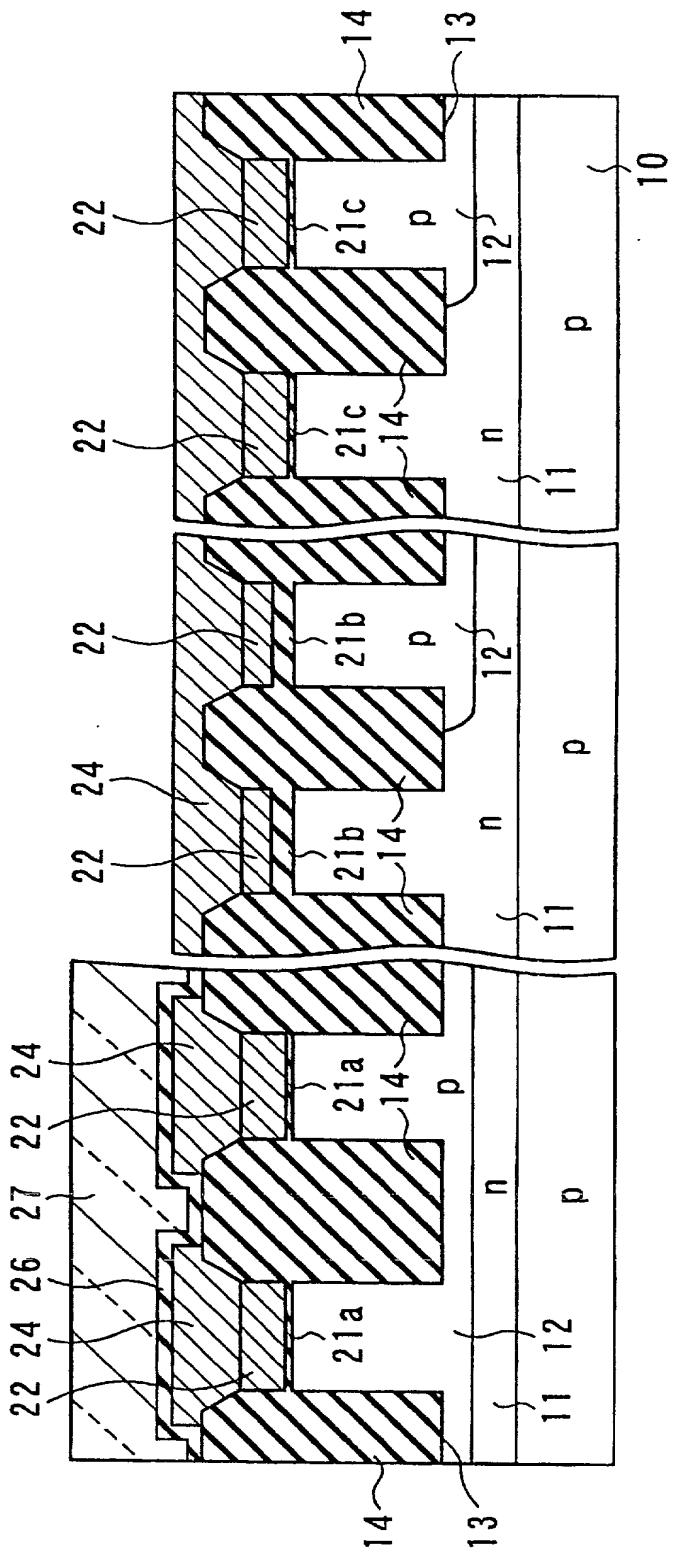


[HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

[CELL ARRAY]

FIG. 10

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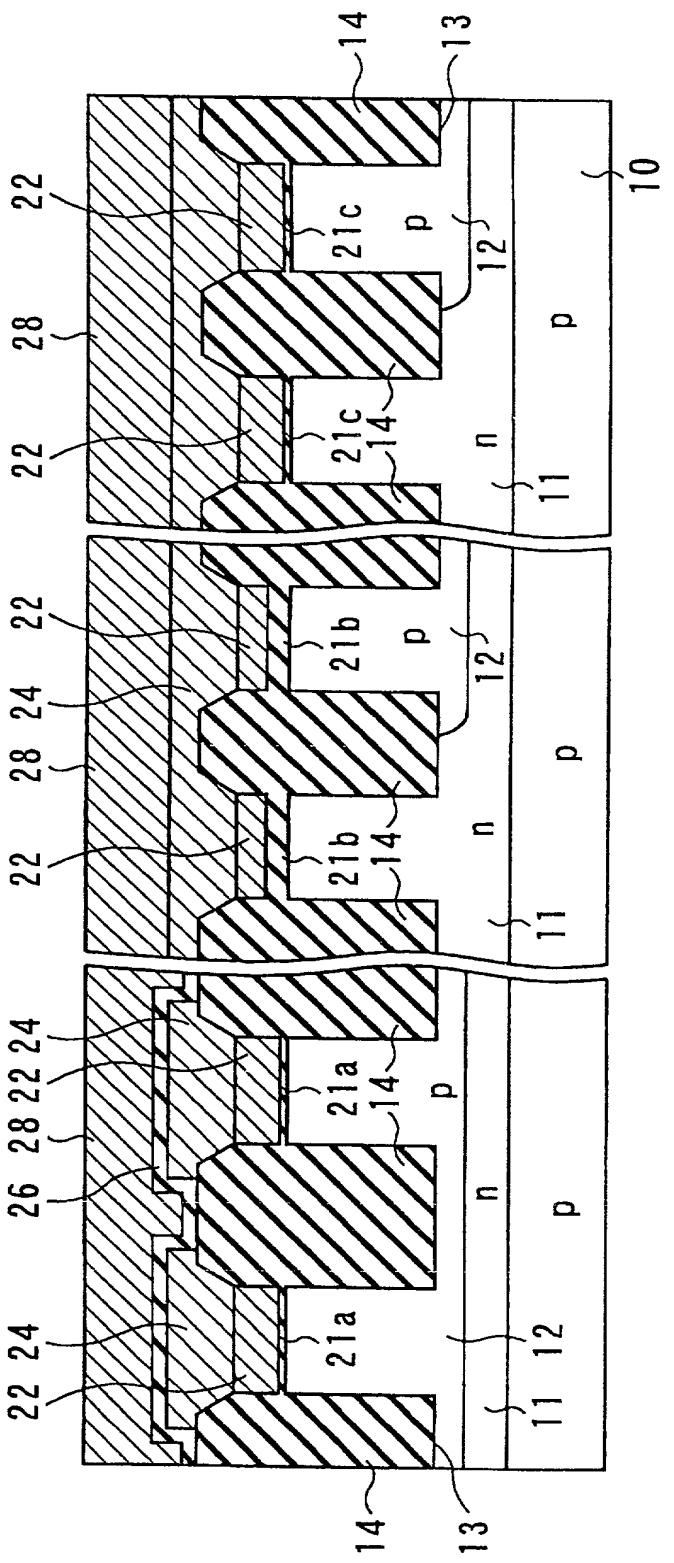


[LOW-VOLTAGE CIRCUIT]

[HIGH-VOLTAGE CIRCUIT]

[CELL ARRAY]

FIG. 11

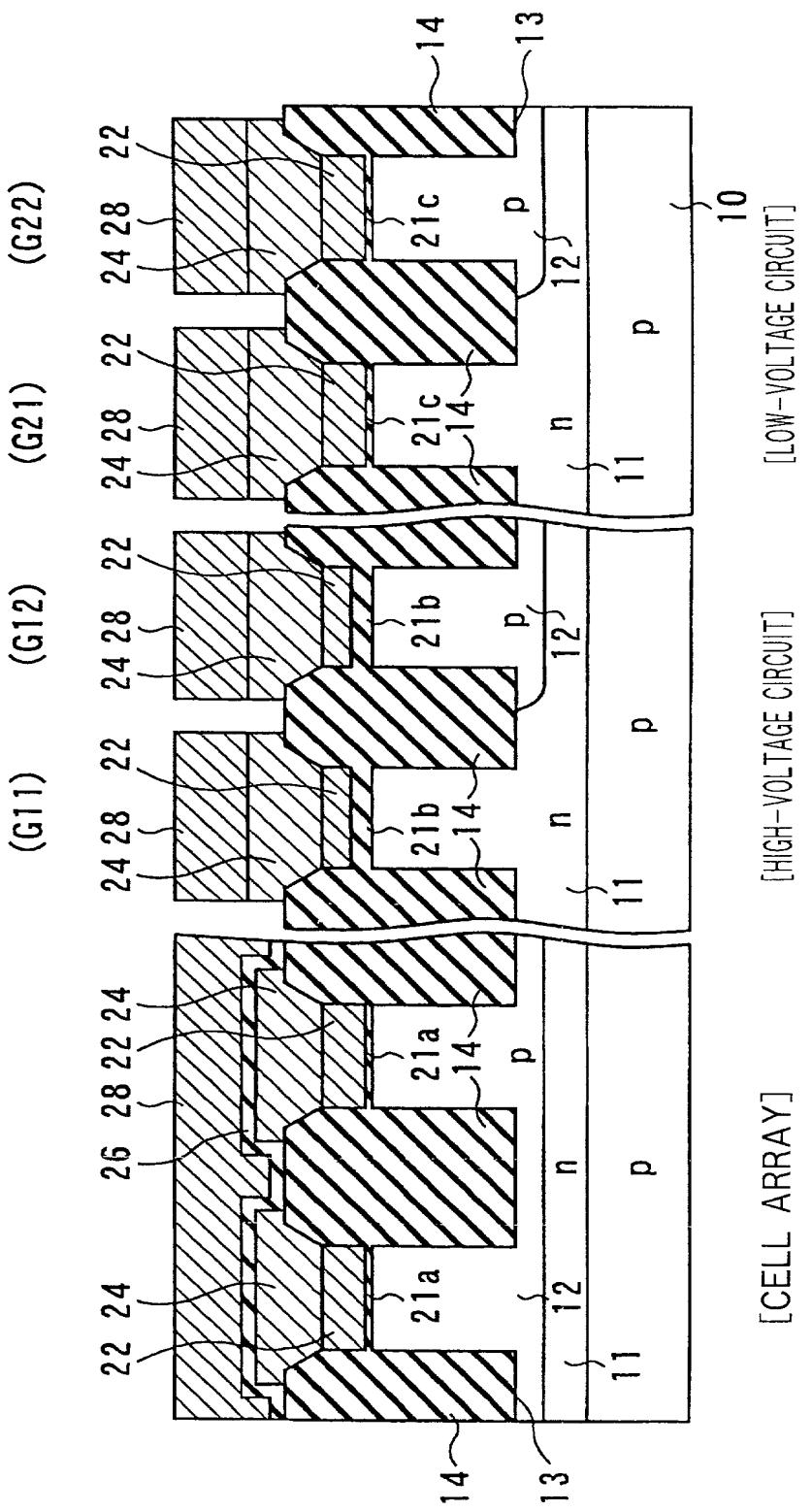


[LOW-VOLTAGE CIRCUIT]

[HIGH-VOLTAGE CIRCUIT]

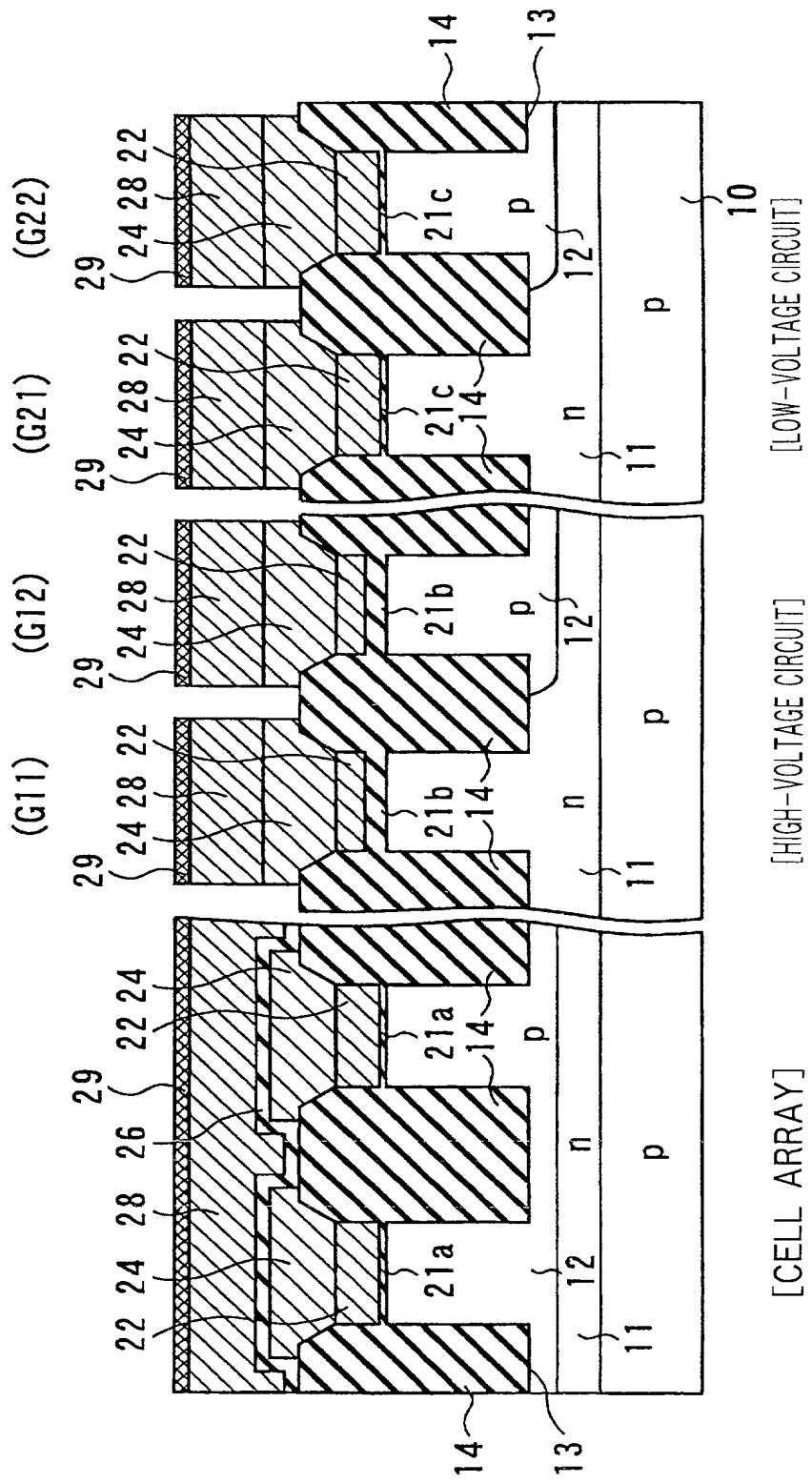
[CELL ARRAY]

FIG. 12



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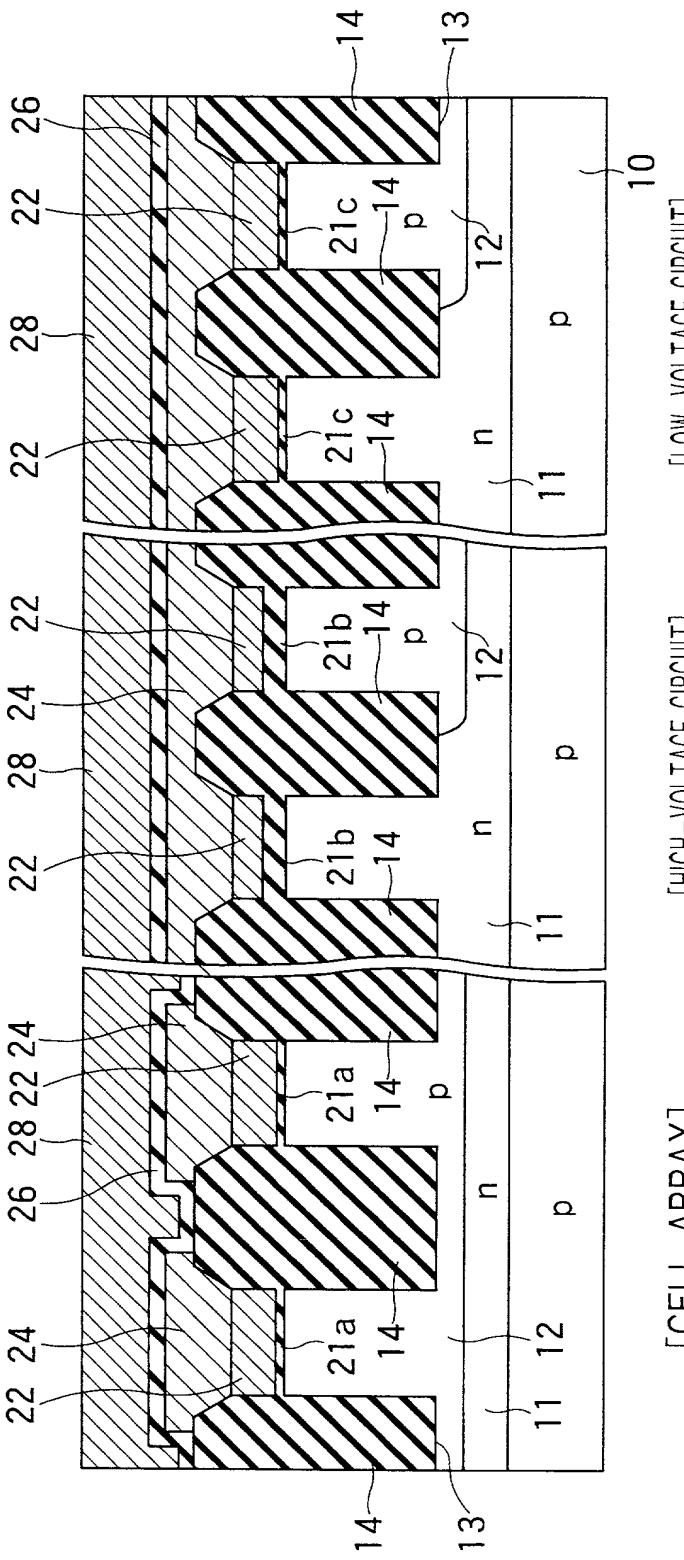


CELL ARRAY [HIGH-VOLTAGE CIRCUIT]

LOW-VOLTAGE CIRCUIT

FIG. 14

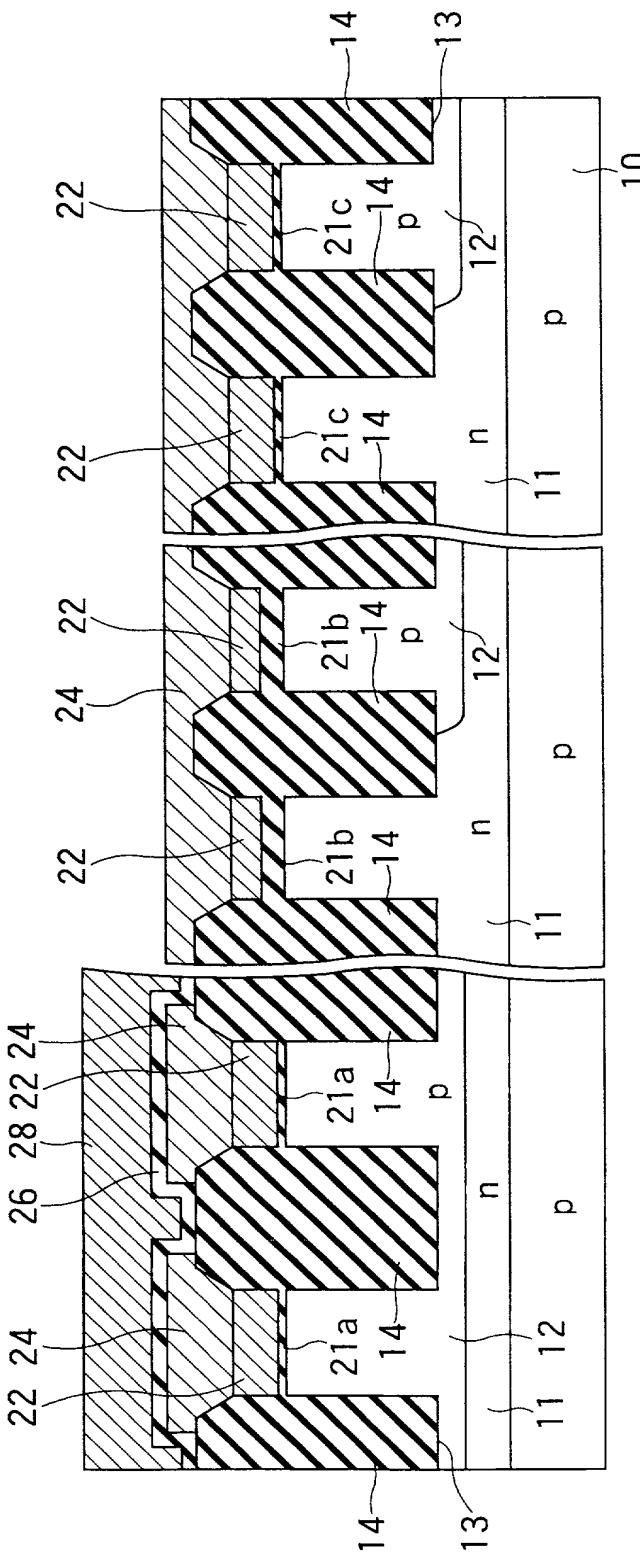
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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 14A

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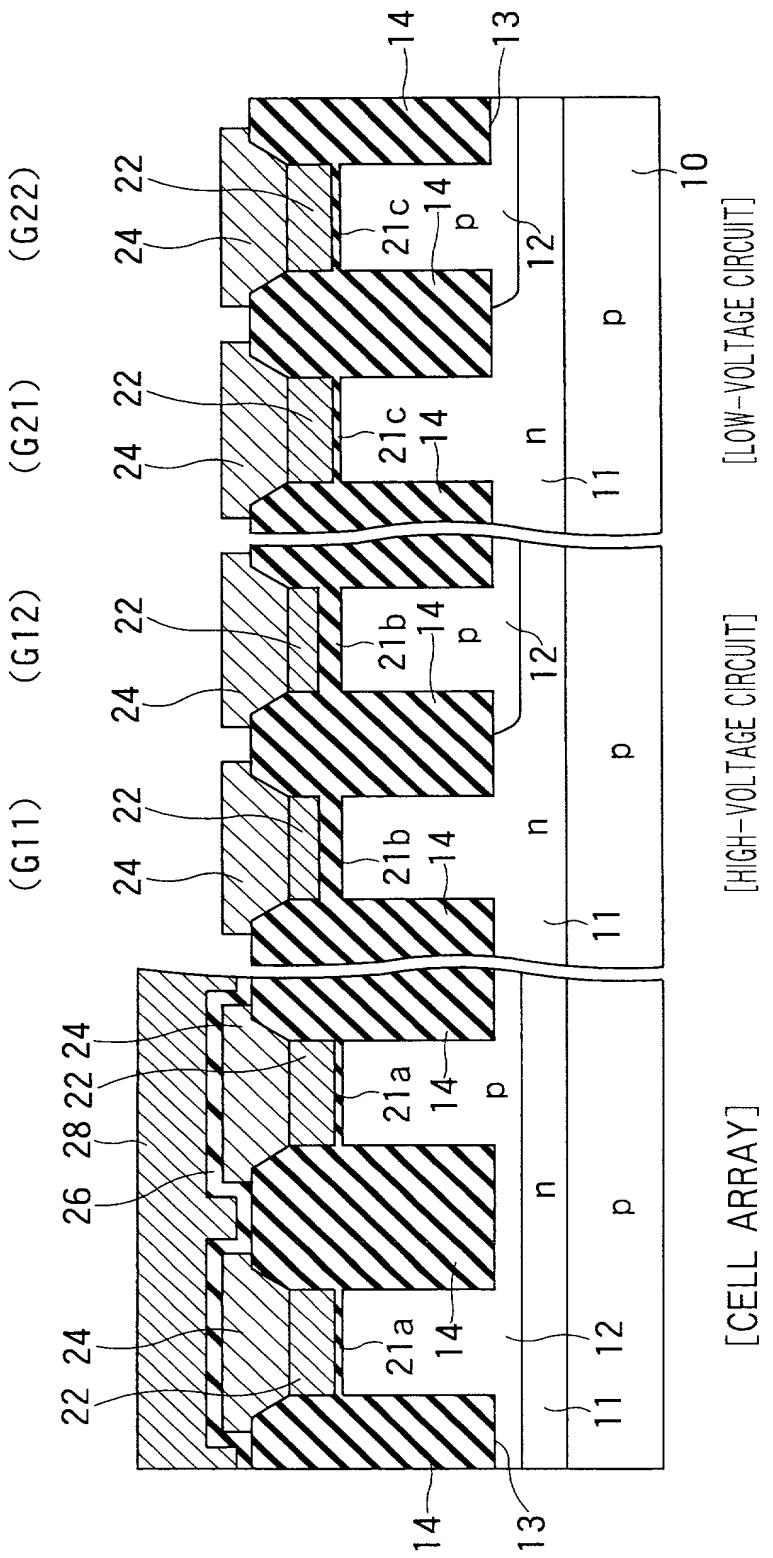
[CELL ARRAY]

[HIGH-VOLTAGE CIRCUIT]

[LOW-VOLTAGE CIRCUIT]

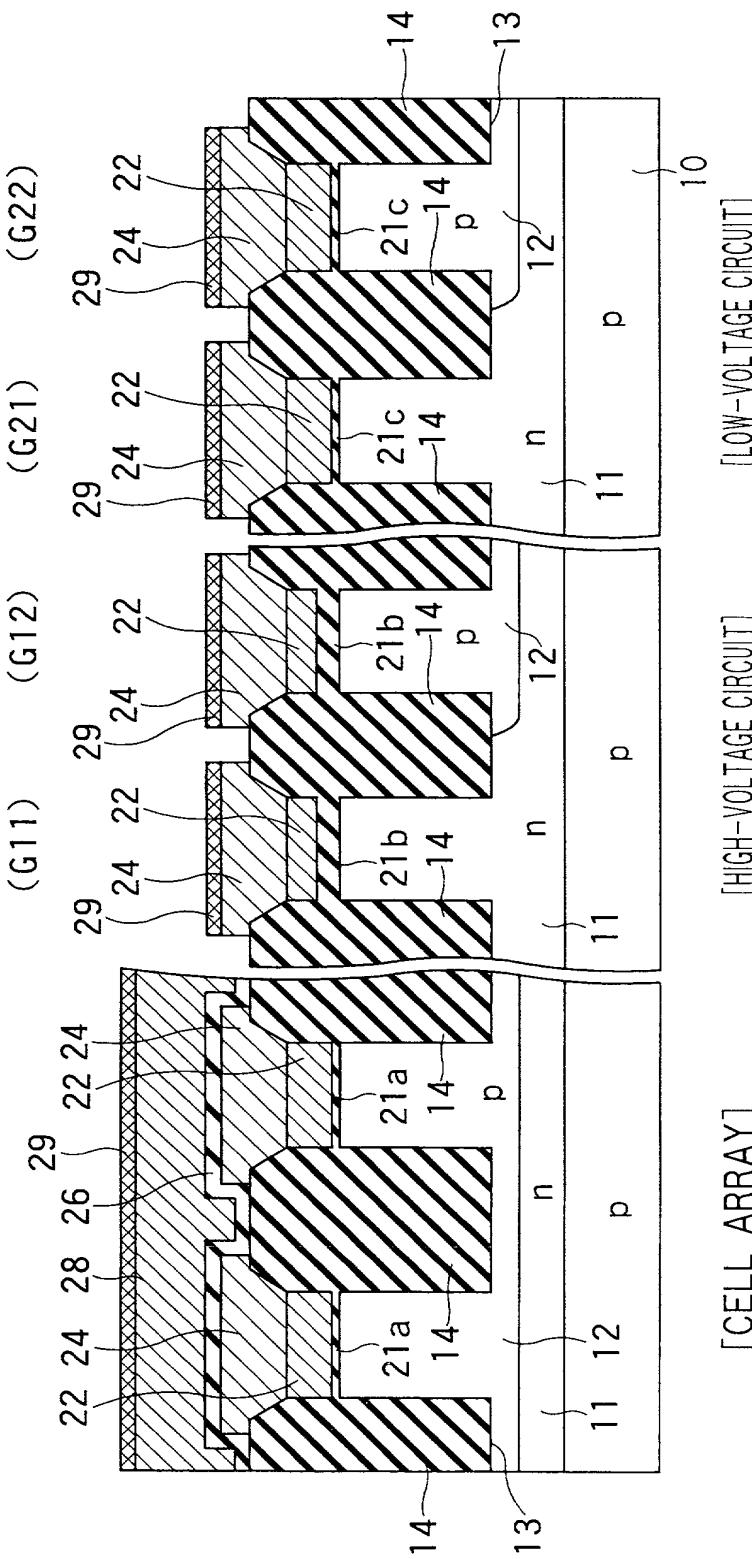
FIG. 14B

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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 14C



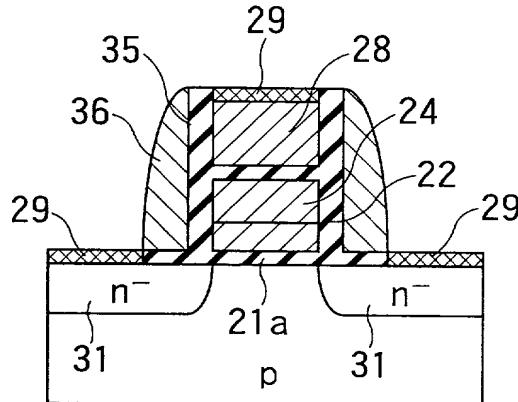
[LOW-VOLTAGE CIRCUIT]

[HIGH-VOLTAGE CIRCUIT]

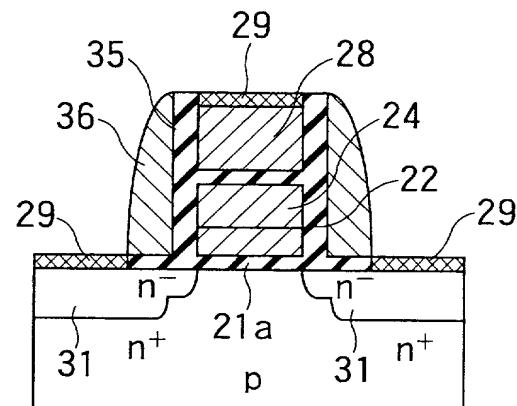
[CELL ARRAY]

FIG. 14 D

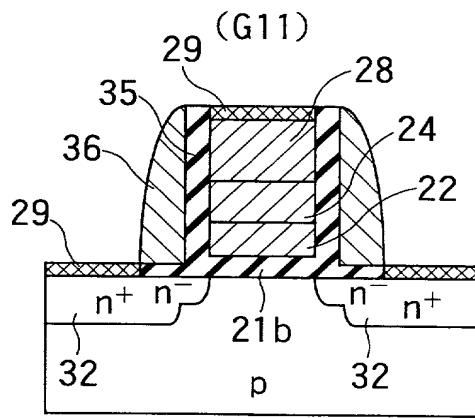
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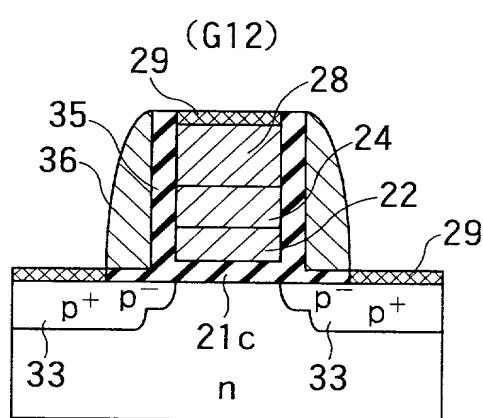
[MEMORY CELL]



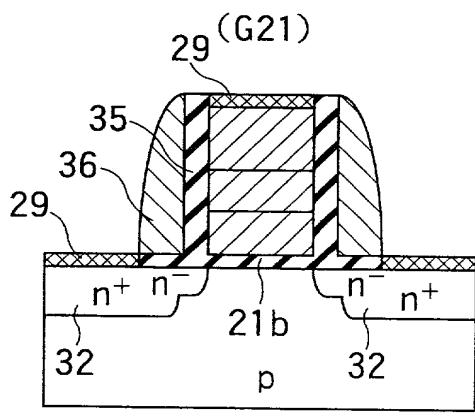
[MEMORY CELL]



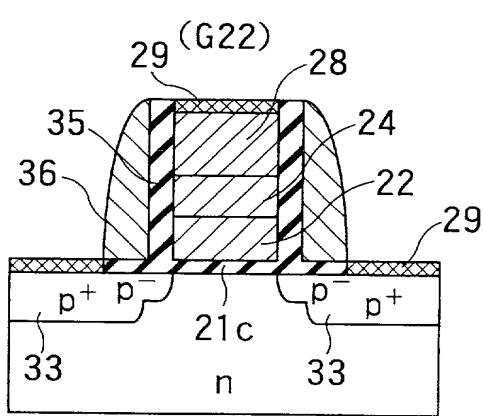
[HIGH-VOLTAGE NMOS]



[HIGH-VOLTAGE PMOS]



[LOW-VOLTAGE NMOS]



[LOW-VOLTAGE PMOS]

FIG. 15

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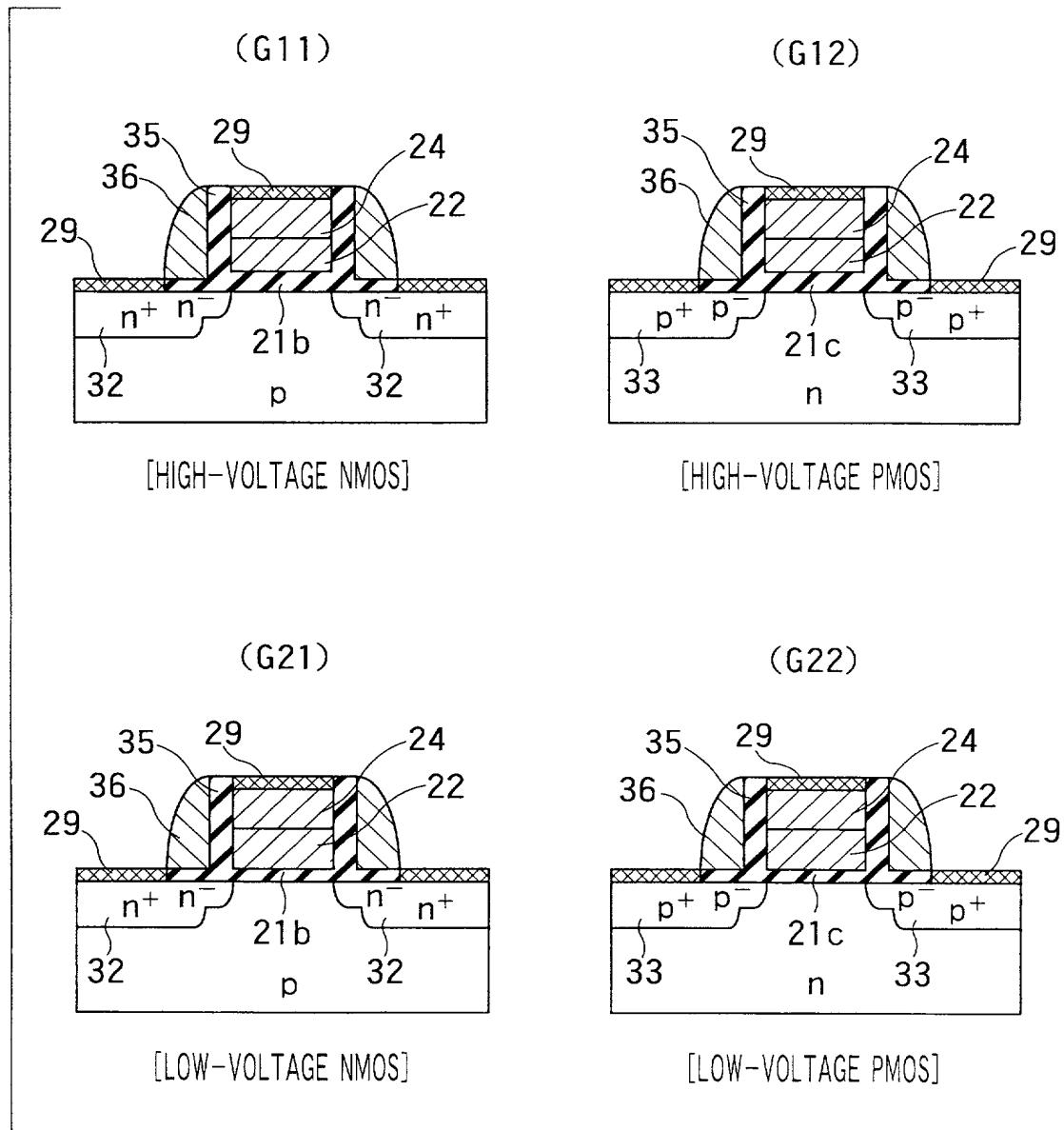
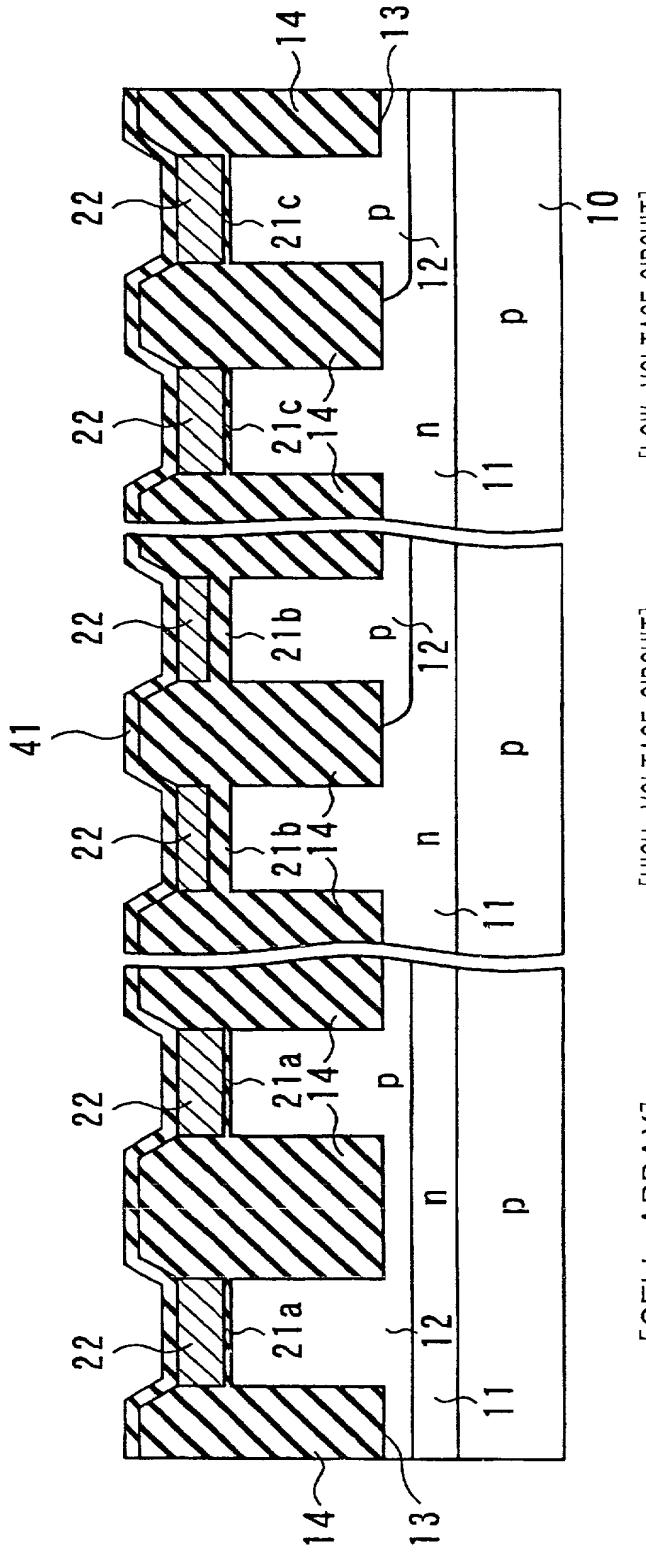


FIG. 15A

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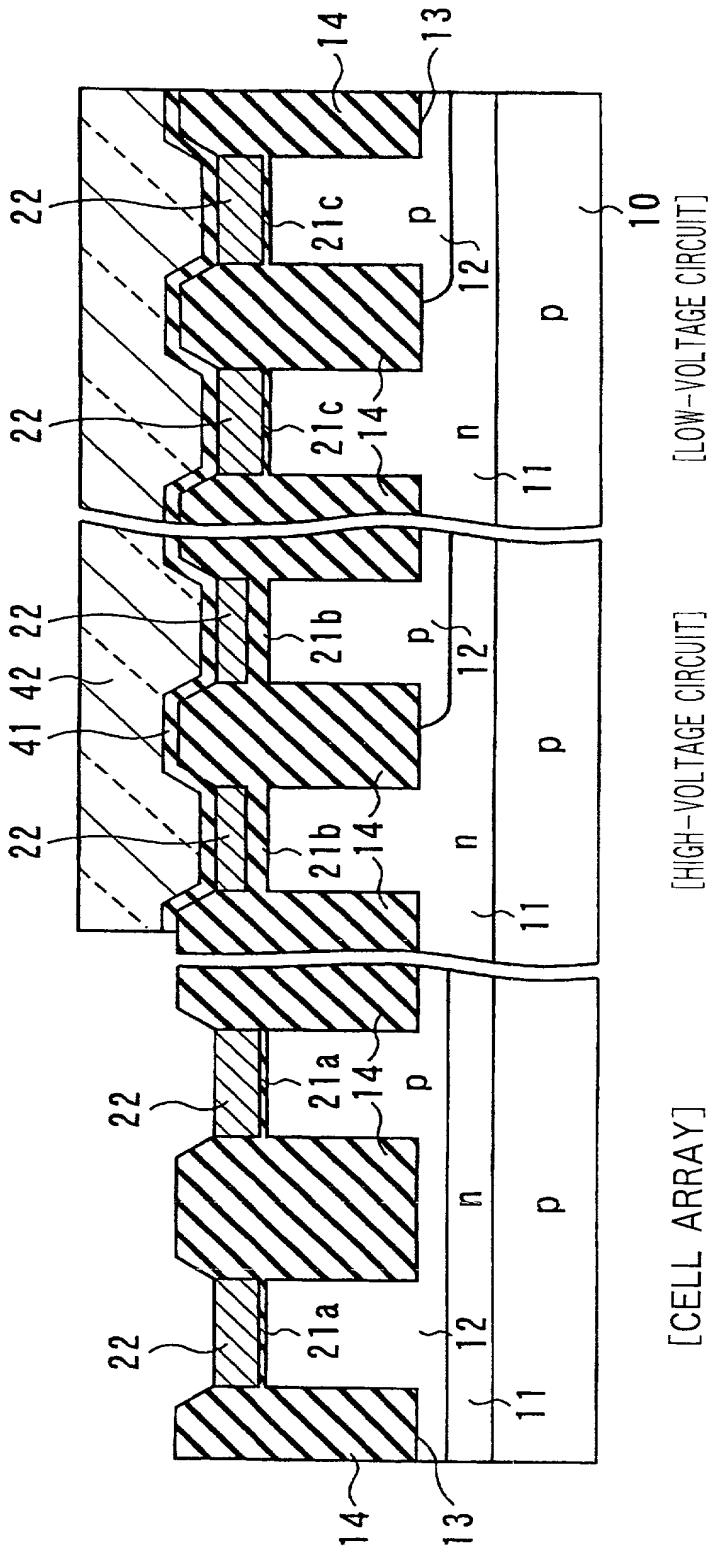
[LOW-VOLTAGE CIRCUIT]

[HIGH-VOLTAGE CIRCUIT]

[CELL ARRAY]

FIG. 16

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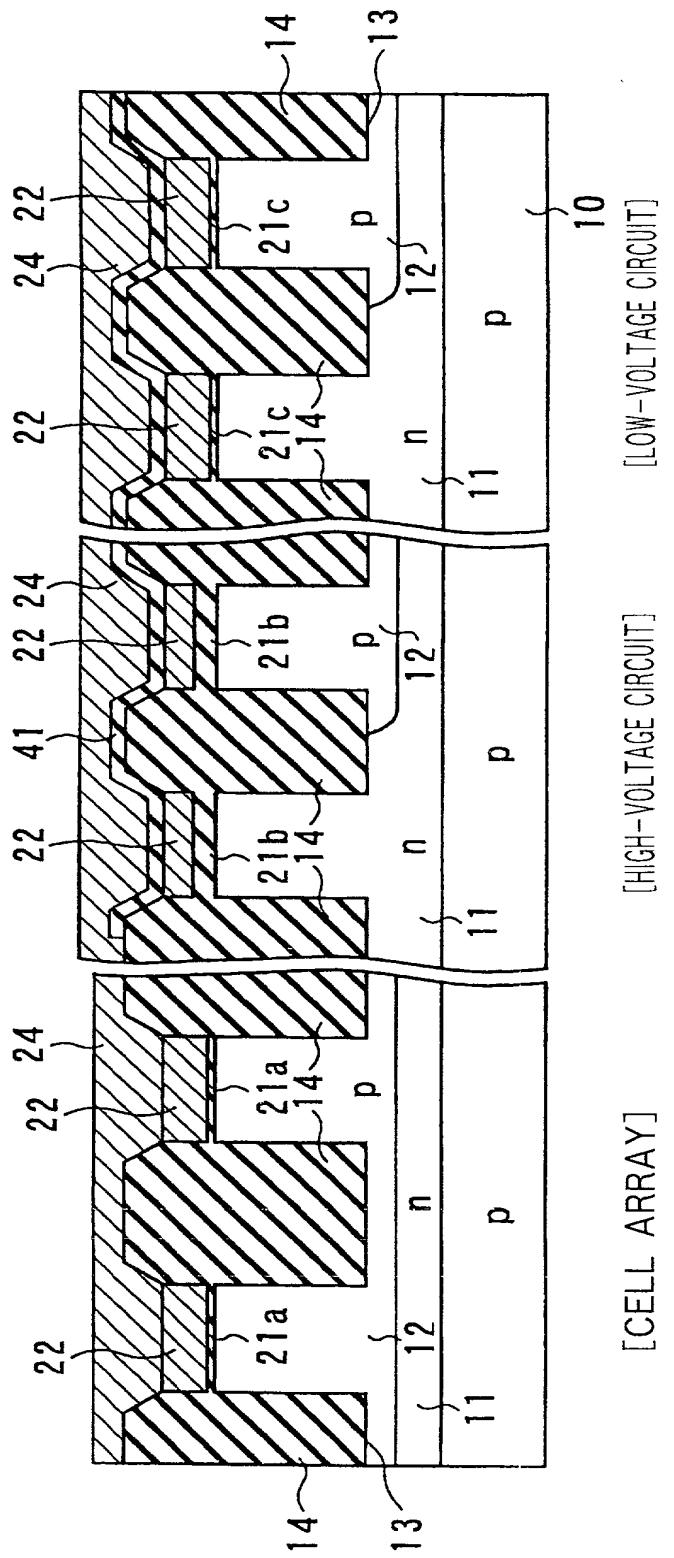


[CELL ARRAY]
[HIGH-VOLTAGE CIRCUIT]

10
[LOW-VOLTAGE CIRCUIT]

FIG. 17

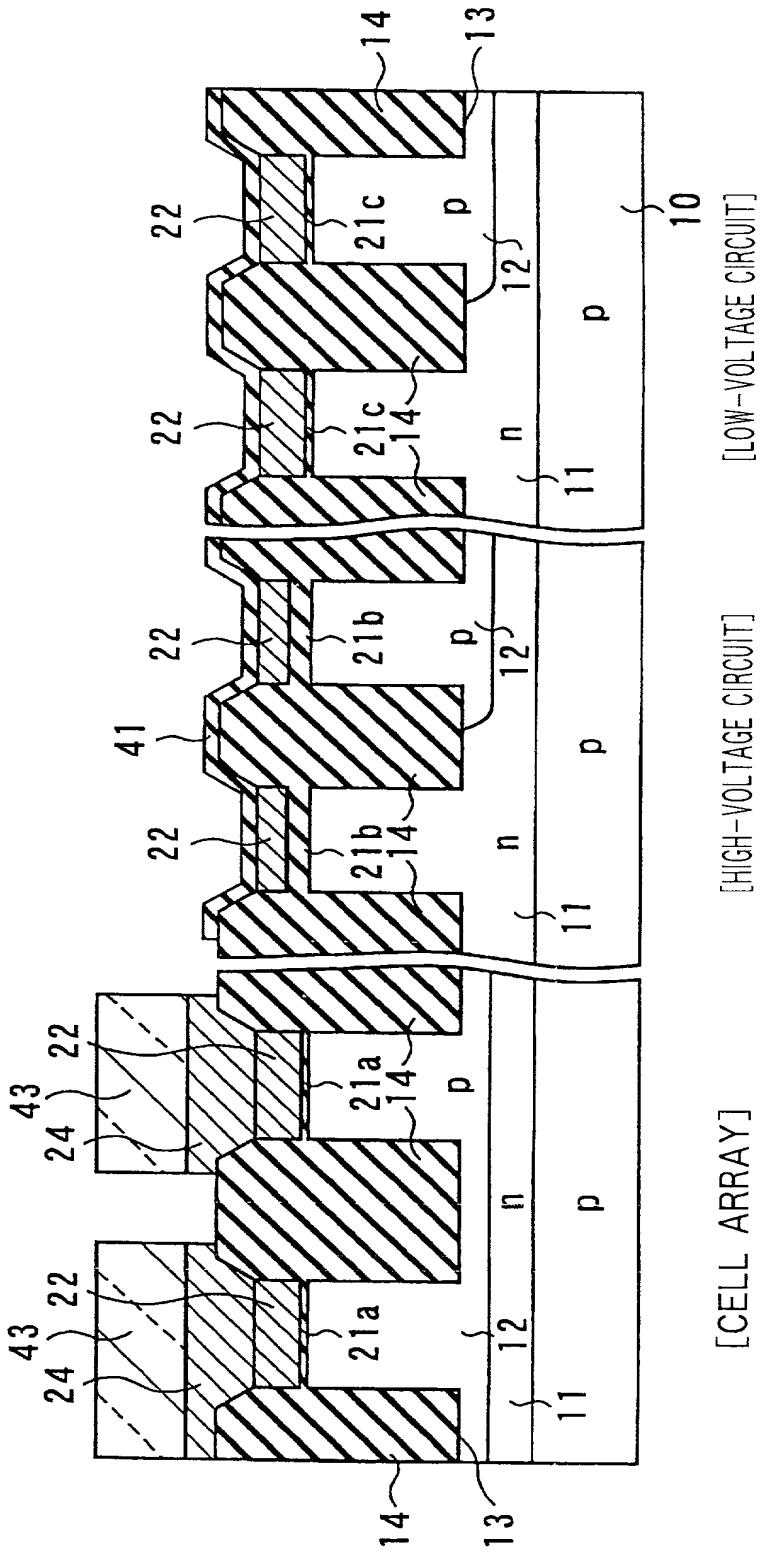
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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 18

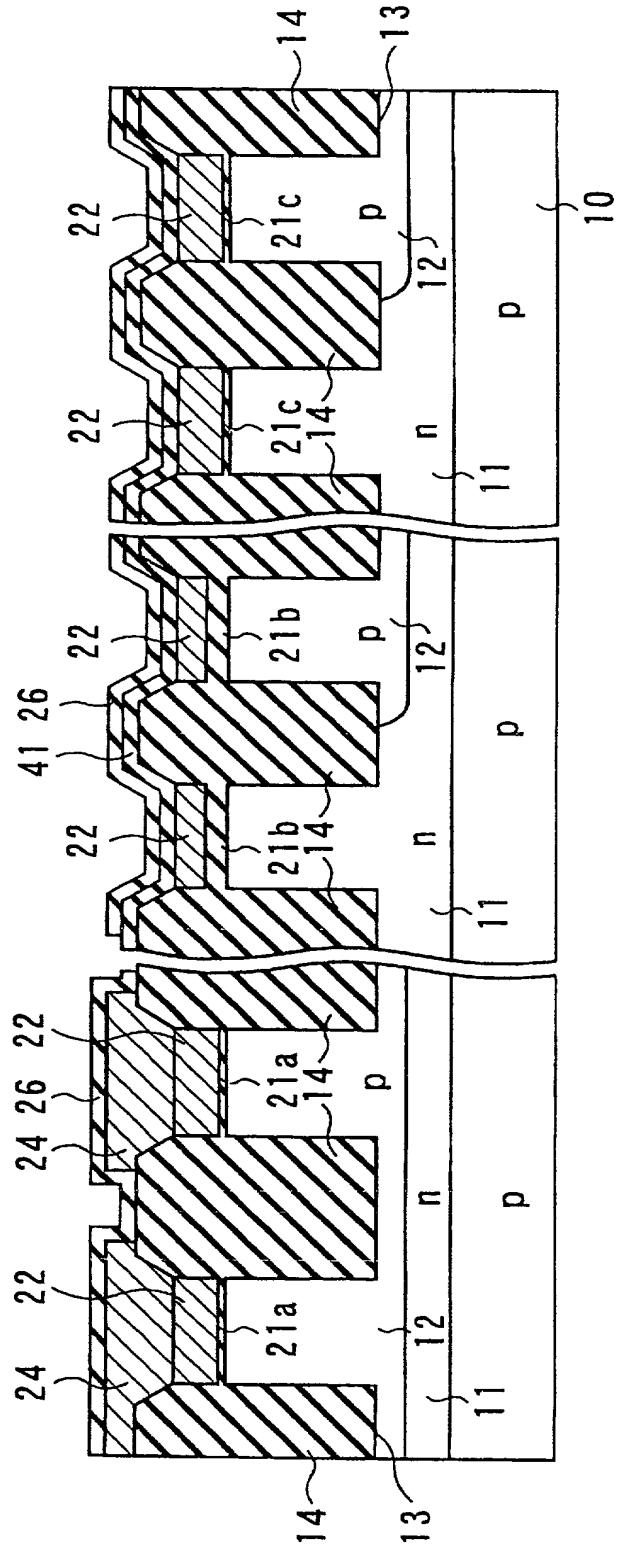
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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 19

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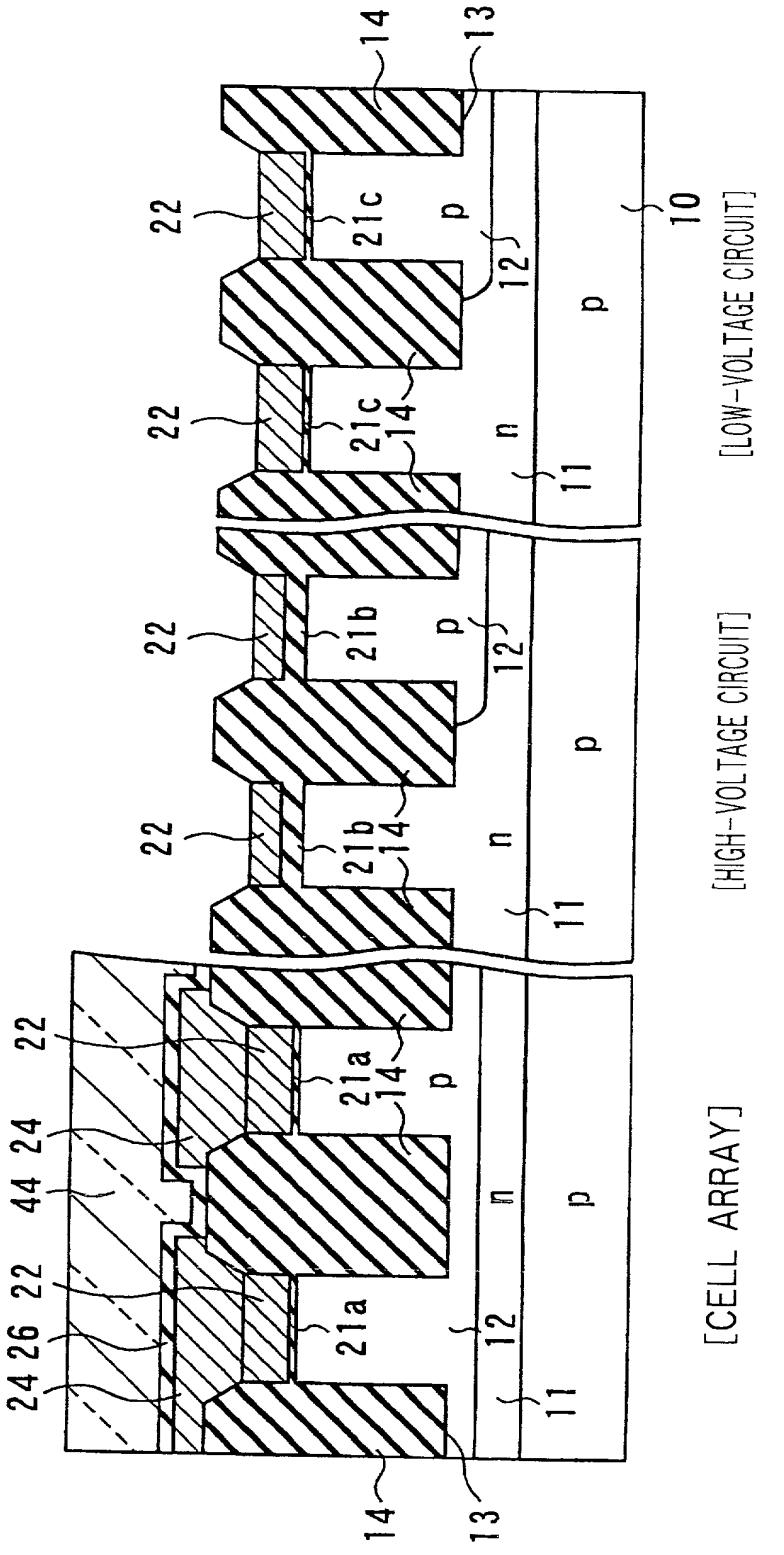


[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT]

[LOW-VOLTAGE CIRCUIT]

FIG. 20

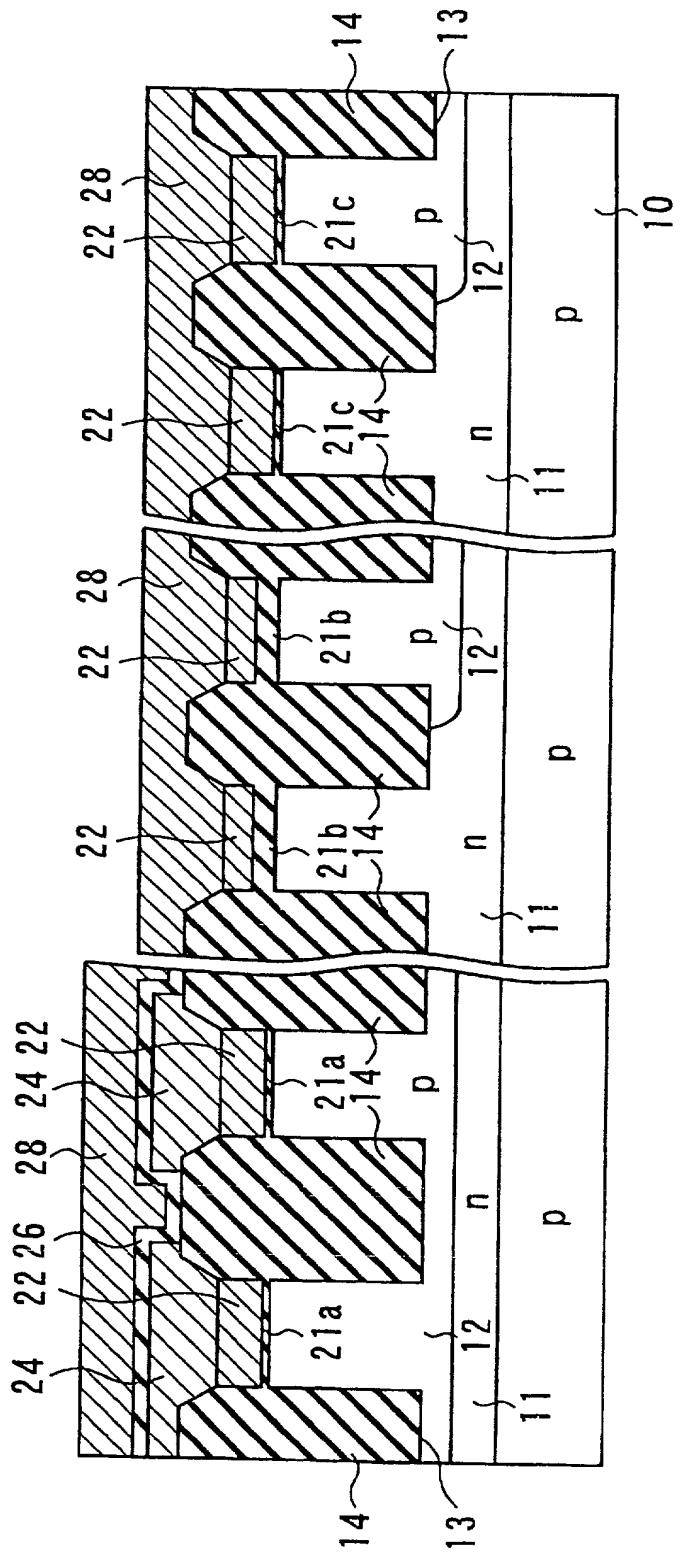
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[CELL ARRAY]
[HIGH-VOLTAGE CIRCUIT]
[LOW-VOLTAGE CIRCUIT]

FIG. 21

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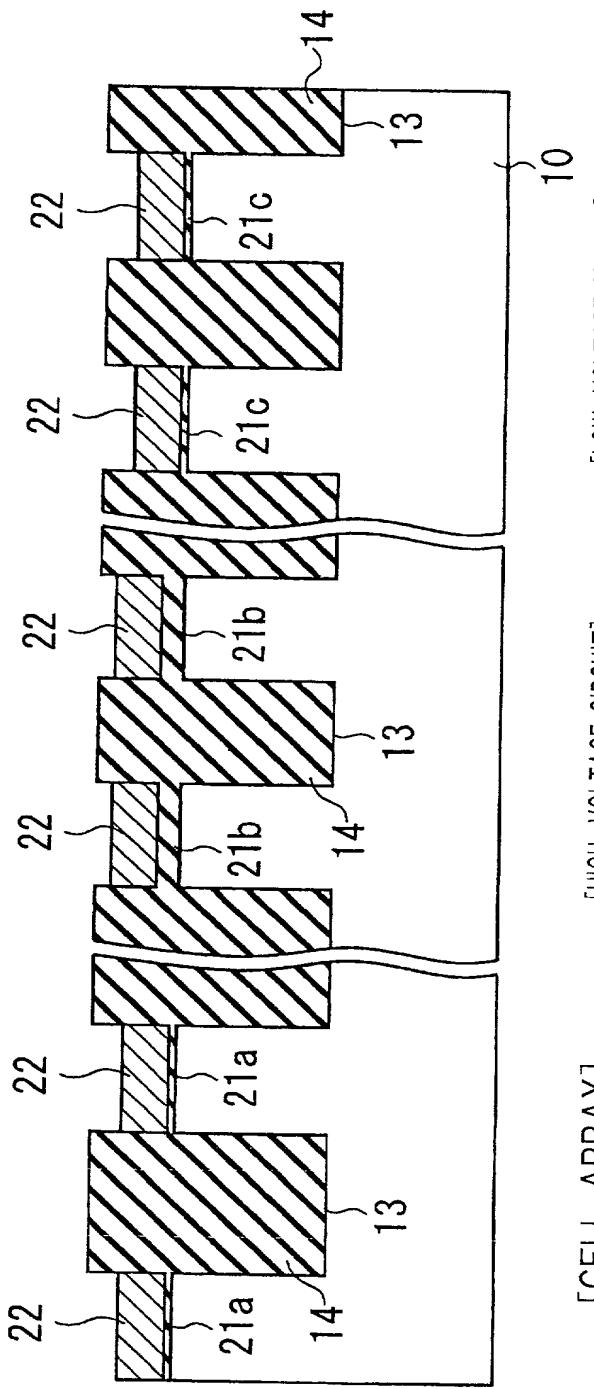
[LOW-VOLTAGE CIRCUIT]

[HIGH-VOLTAGE CIRCUIT]

[CELL ARRAY]

FIG. 22

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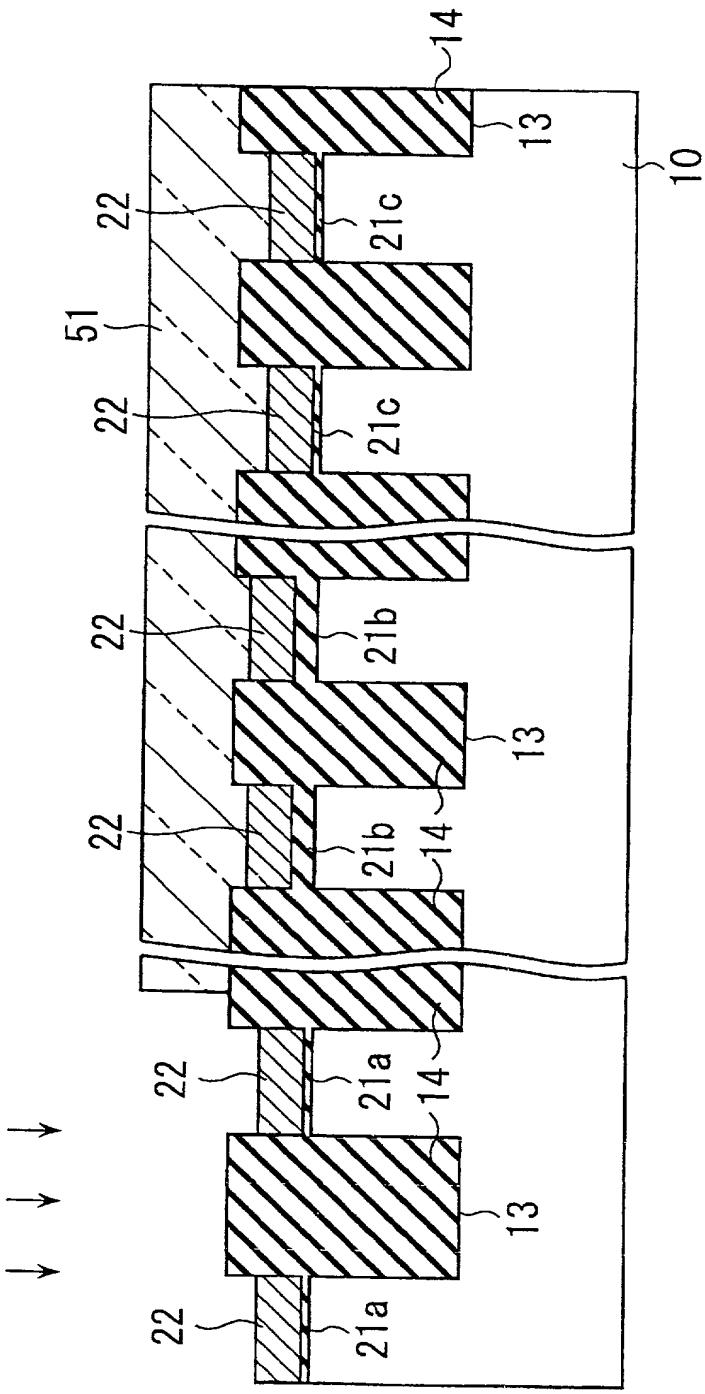


[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 23

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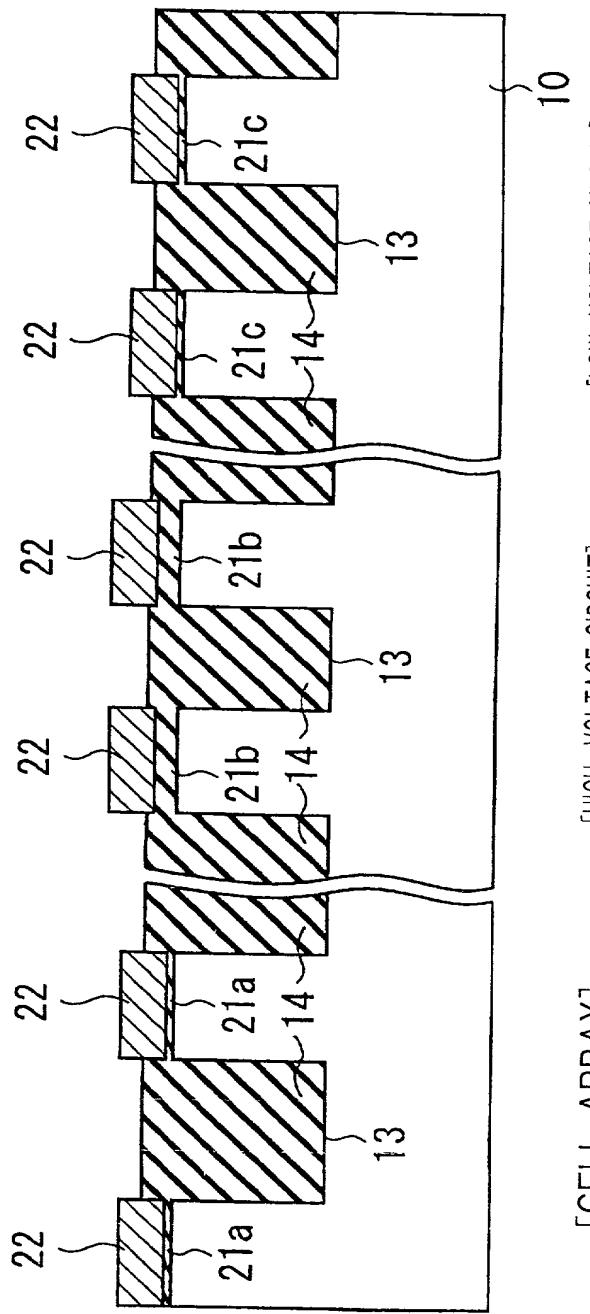
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ION-IMPLANTATION



10
[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 24

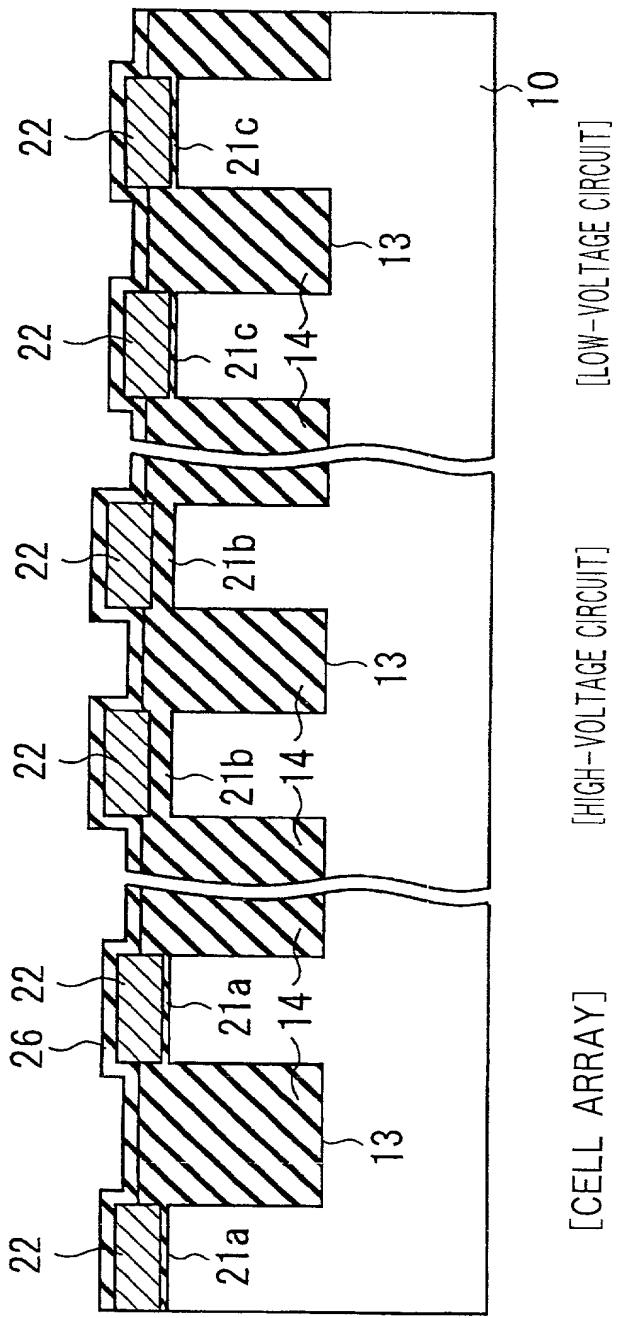
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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT]

[LOW-VOLTAGE CIRCUIT]

FIG. 25



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FIG. 26

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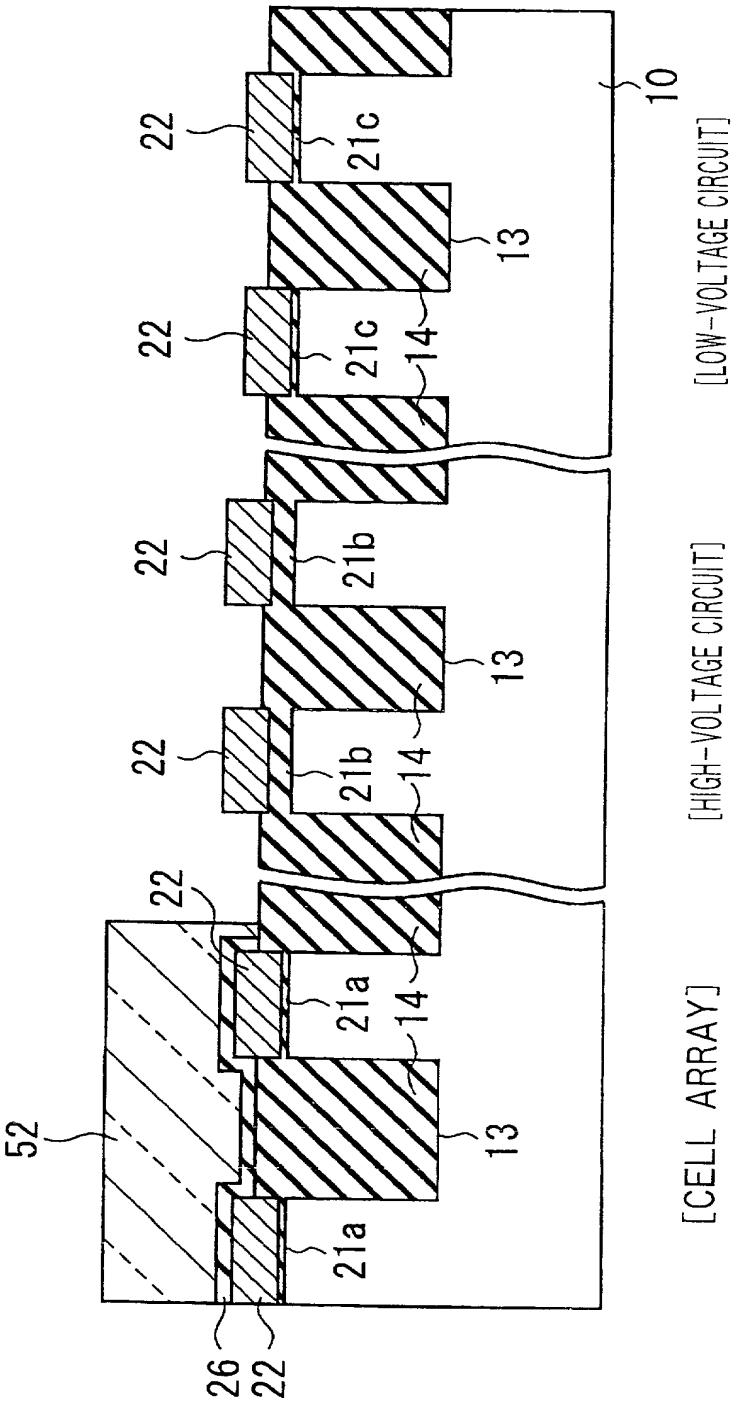
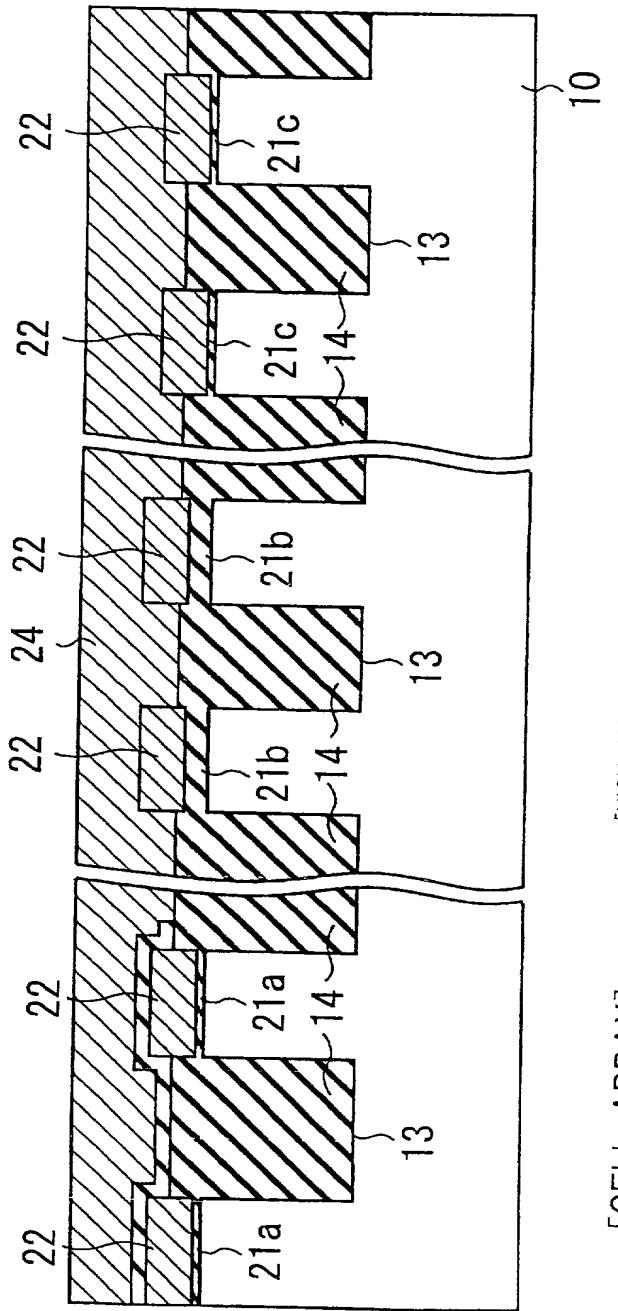


FIG. 27

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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 28

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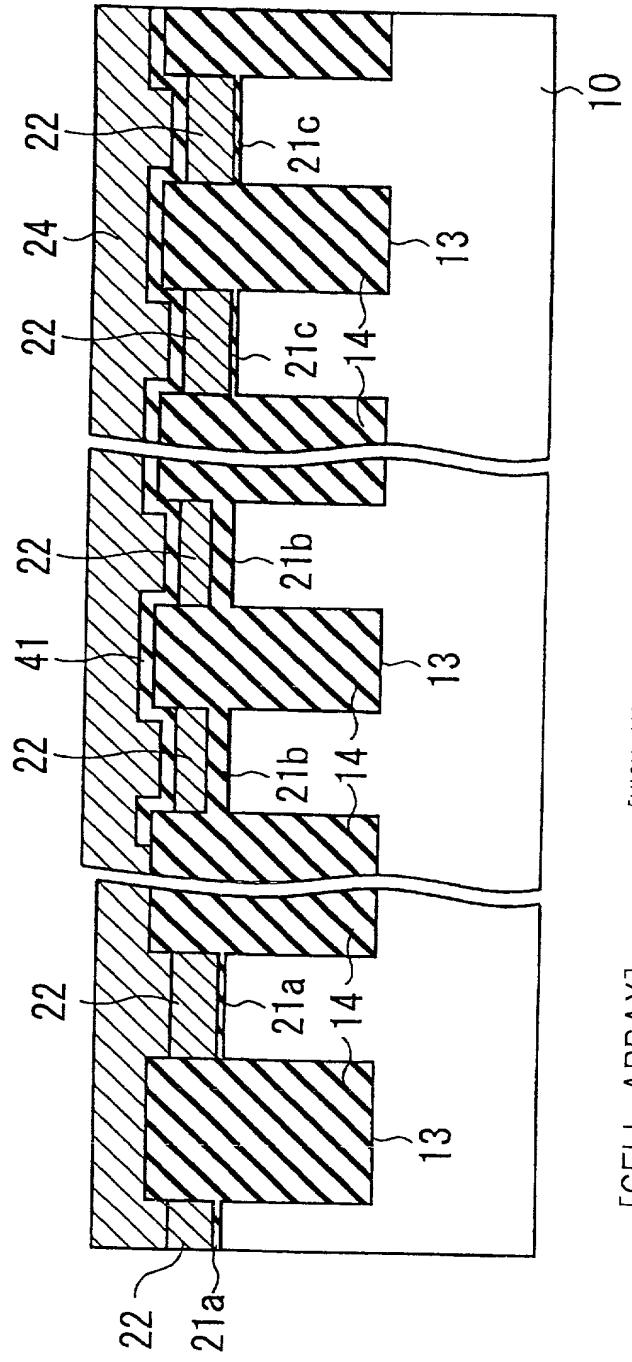
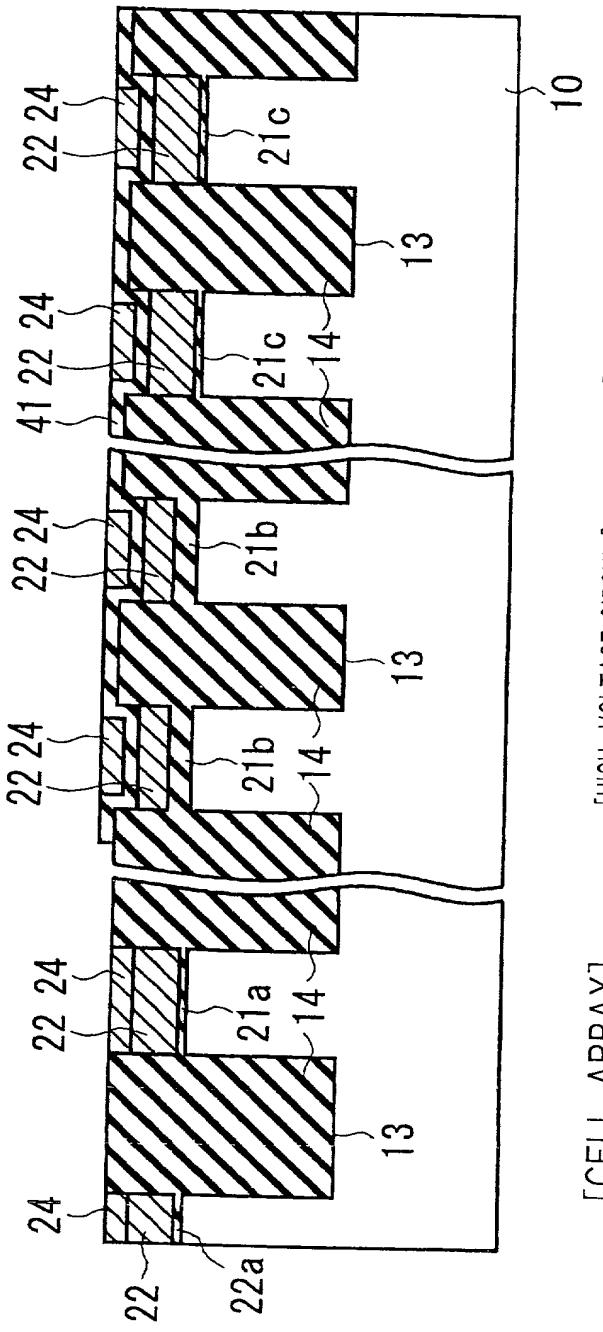


FIG. 29

[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

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[LOW-VOLTAGE CIRCUIT]

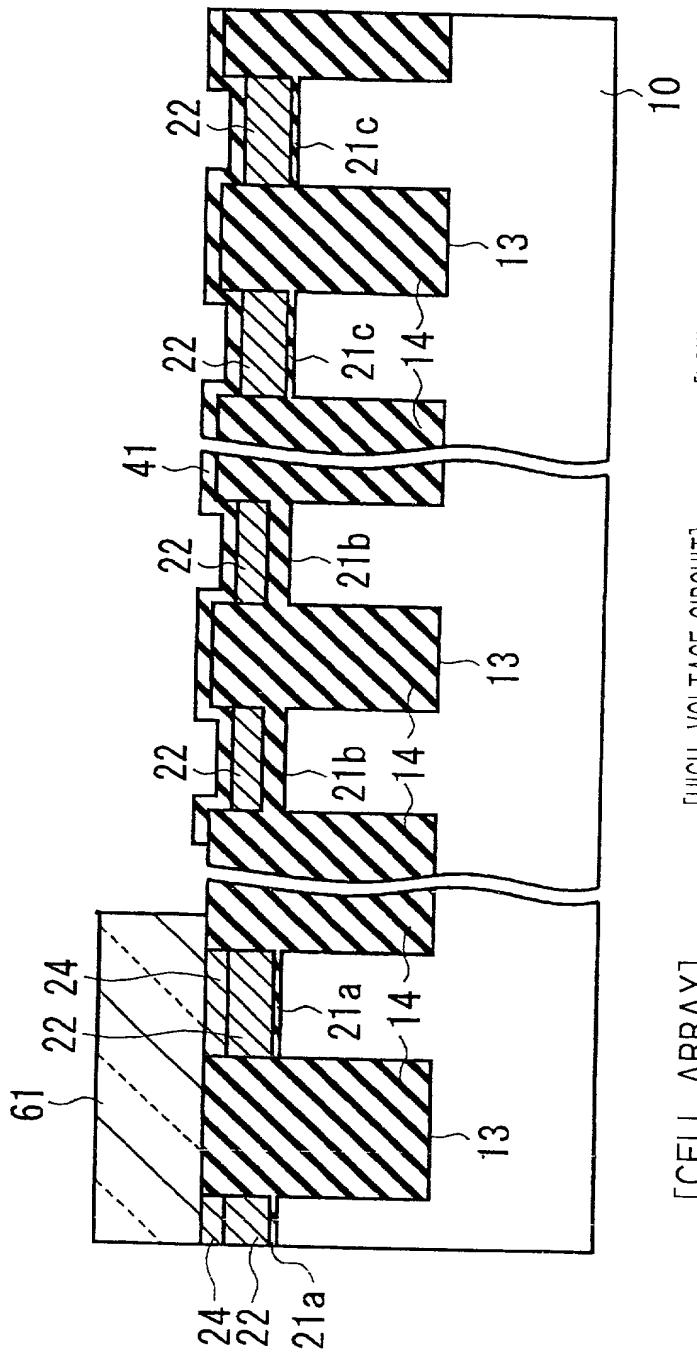
[HIGH-VOLTAGE CIRCUIT]

[CELL ARRAY]

FIG. 30

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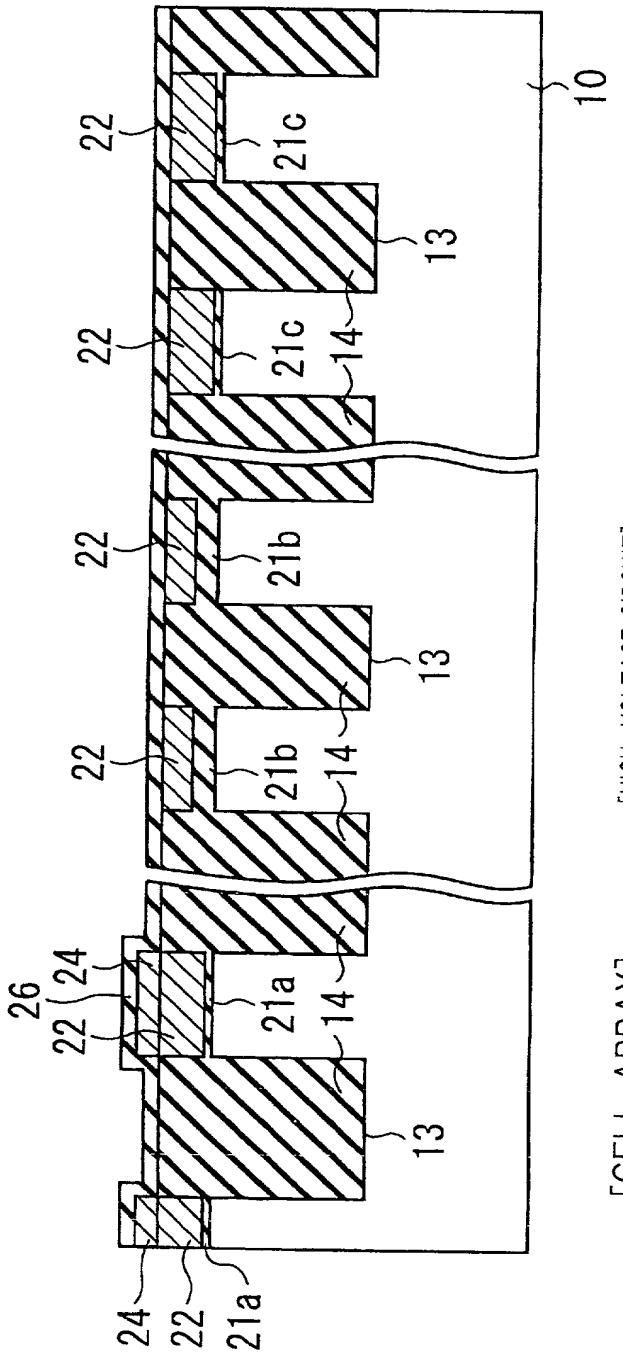


[CELL ARRAY]
[HIGH-VOLTAGE CIRCUIT]

10

FIG. 31

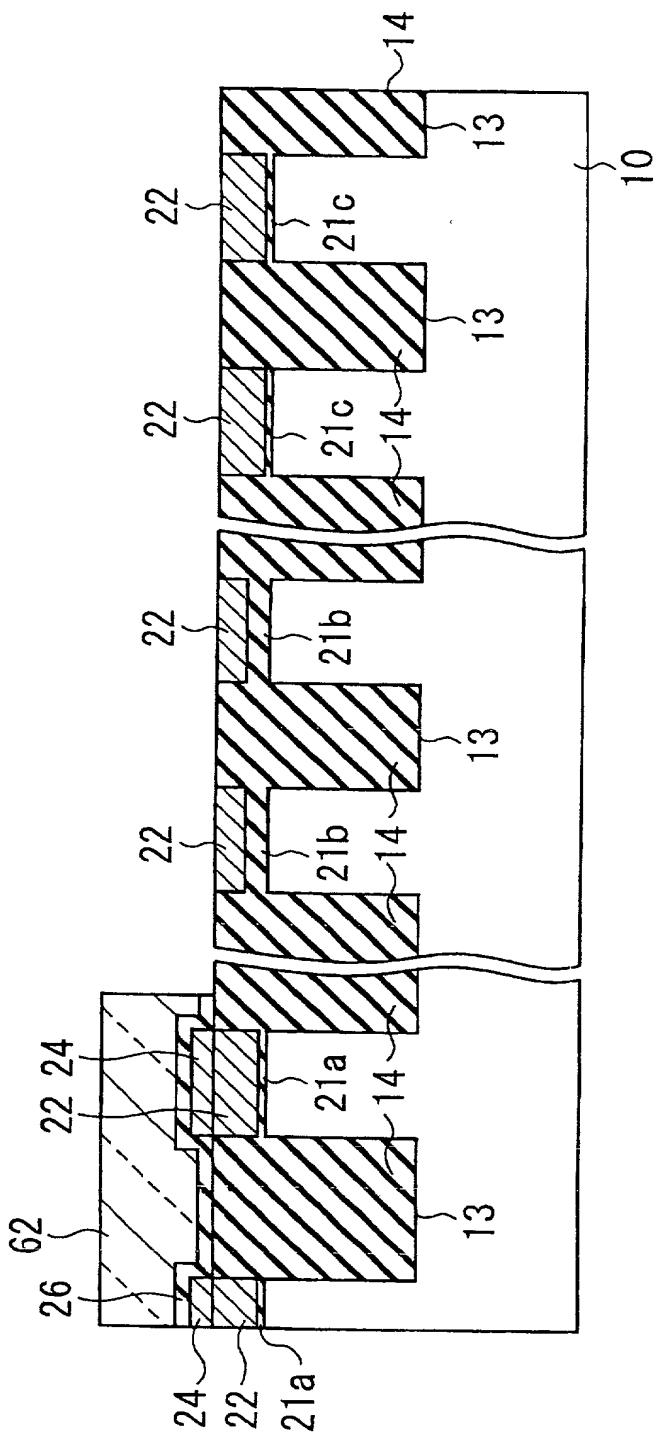
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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 32

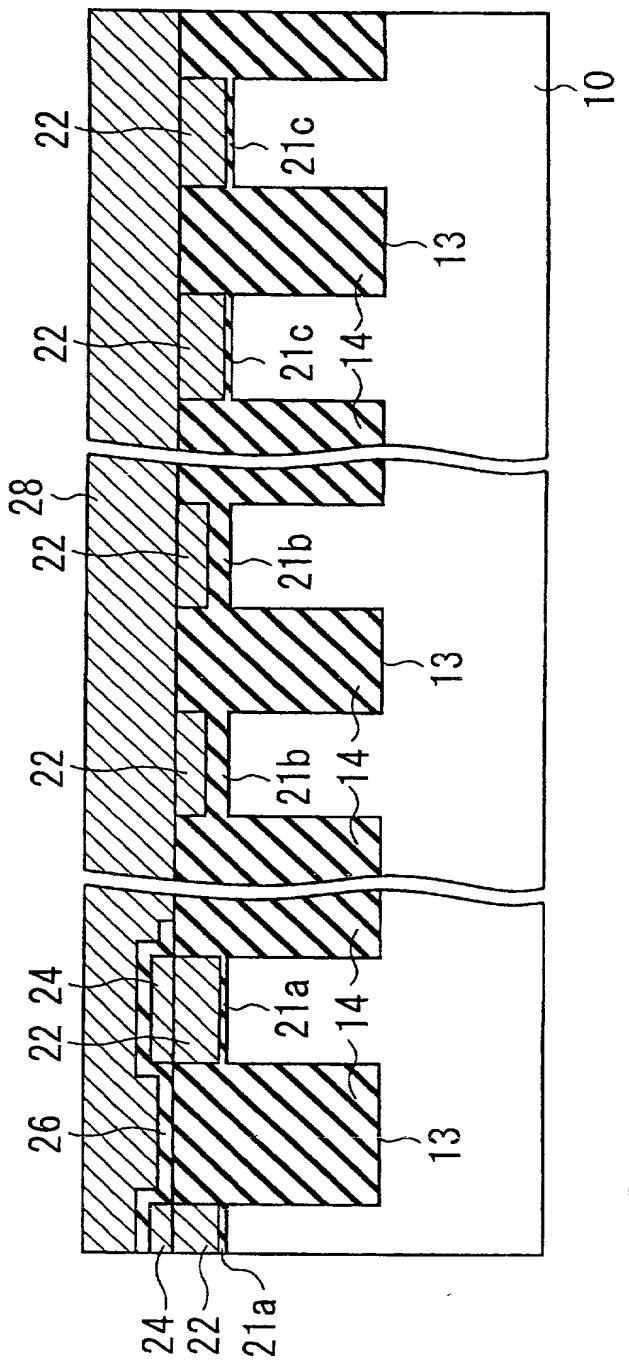
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[CELL ARRAY] [HIGH-VOLTAGE CIRCUIT] [LOW-VOLTAGE CIRCUIT]

FIG. 33

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[CELL ARRAY]
[HIGH-VOLTAGE CIRCUIT]
[LOW-VOLTAGE CIRCUIT]

[HIGH-VOLTAGE CIRCUIT]

FIG. 34

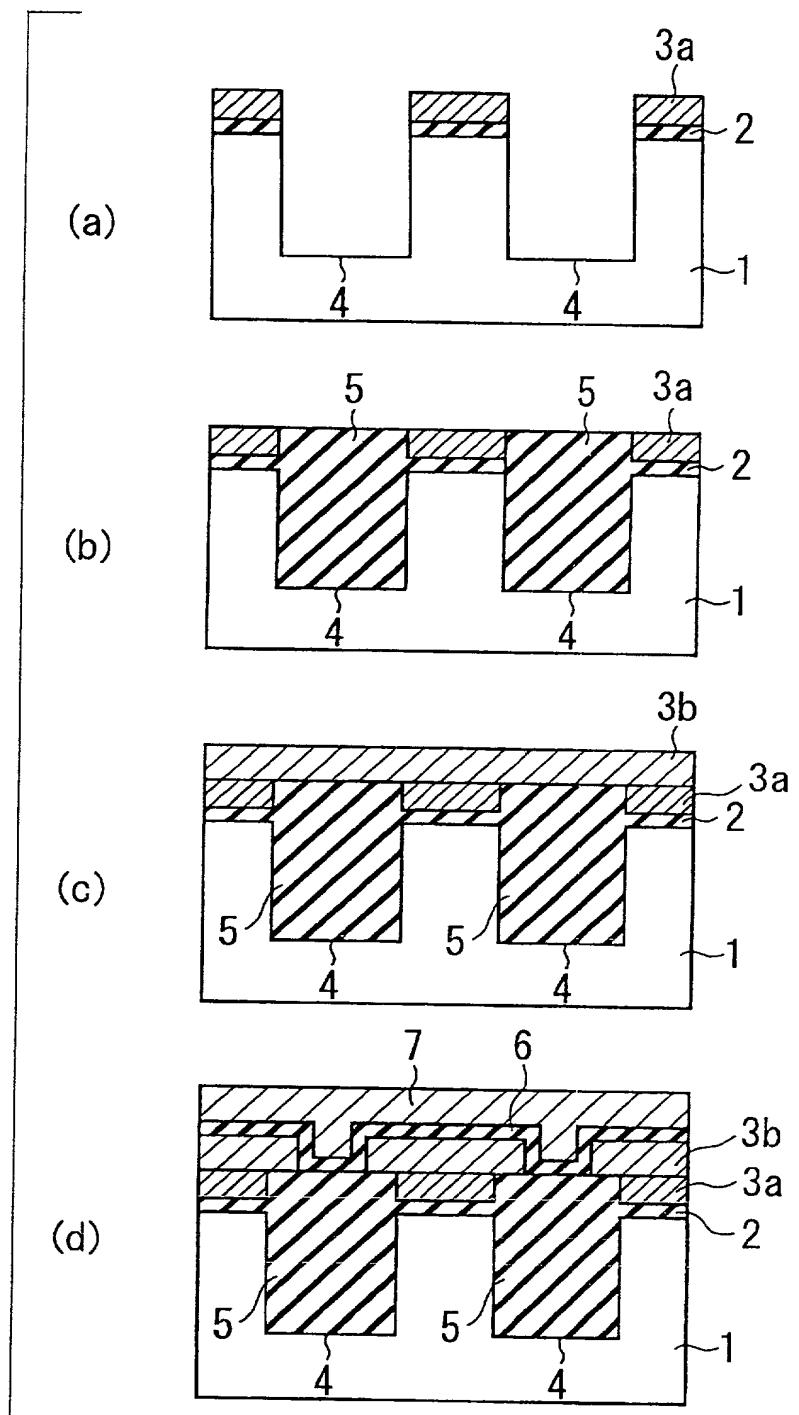


FIG. 35

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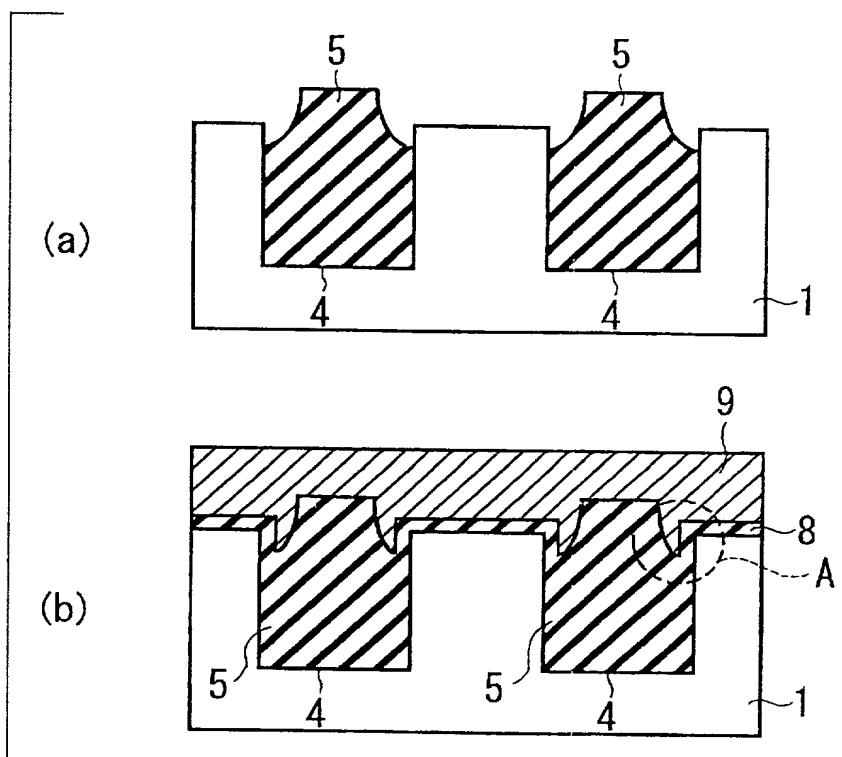


FIG. 36